Field-effect transistors (FETs) based on semiconducting single-walled carbon nanotubes (sSWNTs) are one of the most promising building blocks for the generation of superfast electronic devices. Among the proposed nanocarbon based materials, with very high carrier mobilities, sSWNTs are the only true semiconductor. Their band gap (∼0.7 eV) is, in fact, compatible with the integration in logic circuits, since it allows FETs based on sSWNTs to be turned off. Nevertheless, the fabrication of sSWNT FETs remains a great challenge. Until now, high performance ambipolar devices combining a high carrier mobility and a high on/off ratio were only achieved with sSWNTs grown by chemical vapor deposition (CVD) on a rigid substrate at high temperatures (∼900 K). Furthermore, those devices were fabricated by serially patterning the electrode by electron beam lithography on the selected sSWNT (single strand or network). These processes are expensive, low throughput, energy demanding, and currently unsuitable for the demands of large area and flexible device integration.

The ability to disperse SWNTs in water and organic solvents makes solution-based processes more attractive for the fabrication of sSWNT based devices. The related solution-based techniques, i.e., drop-cast, self-assembly (evaporative, Langmuir–Blodgett, DNA-assisted), and inkjet printing, allow large area device preparation at room temperature that is well suited for device integration. Nevertheless, many challenges remain to achieve a comparable performance with those obtained with the “ideal” sSWNT FET, fabricated by the aforementioned CVD method. In general, the devices produced by solution-based methods are characterized by low carrier mobilities because of the sparse coverage of the SWNTs on the FET channel and the random orientation of the nanotube network. Although some improved carrier mobilities were reported by increasing the nanotube density or by aligning the SWNTs on films, these devices have shown low on/off ratios due to the remaining metallic tubes content. The reduction of the on/off ratio compromised the merit of sSWNT over competing nanocarbon FETs, especially when considering the short channel length. Therefore, purifying and sorting sSWNTs from any remaining metallic SWNTs is of paramount importance.

Several recent reports showed that conjugated polymers (e.g., poly-9,9-di-octyl-fluorenyl-2,7-diyl (PFO)) can be effectively used to prepare sSWNT dispersions since their extensive π-conjugated structure can interact strongly with the nanotube. However, fabricating high performance devices from the sSWNT dispersions still remains a challenge. It turned out that removal of the polymers is essential to achieve good device performance. Izard et al. reported the separation of dispersed HiPCO SWNTs from excess PFO by extensive washing. The resulting FETs, in which the SWNTs were oriented by dielectrophoresis, showed a relatively good performance but without any ambipolar behavior. In any case, the washing, filtration and iterated rinsing with copious amounts of solvents makes the process laborious and costly.

In this article, we demonstrate a scalable and efficient method for the preparation of high purity sSWNT dispersions, in which the excess polymer can be removed effectively. High performance solution-processed ambipolar sSWNT-FETs are obtained showing electron mobilities around 3 cm²/V·s in randomly oriented sSWNT network FETs, with on/off ratios of 10⁵. Remarkably, this value is even obtained in transistors with very short channel length (5 μm). Moreover, we discuss the influence of the post-treatment on the transport properties of the FETs, and we compare the device performance with the other nanotube network FETs.

To prepare the sSWNT dispersion, we performed strong sonication of the SWNT/polymer solution followed by ultracentrifugation. We used CoMoCAT SWNTs because of narrower chirality distribution, in comparison with HiPCO SWNTs. Poly(9,9-di-octyl-fluorenyl-2,7-diyl) (PFO) (Mₙ = 58 200 g/mol by GPC) was used as the dispersant. PFO solution in toluene has been proven to be one of the most effective methods for sorting semiconducting SWNTs due to the unique interaction between the polymer chains and semiconducting carbon nanotubes. Dry nanotubes were added to 10 mL of polymer solution in a weight ratio (SWNT/polymer) of 1:3. The mixture was ultrasonicated for 2 h in a high-power water-bath sonicator to produce the as-dispersed solution. After sonication, the crude dispersion was ultracentrifuged at 40 000 rpm (190 000 g) for 1 h. The supernatant (50%) was collected and transferred into new centrifuge tube for a second round of centrifugation in...
In order to enrich sSWNTs and to remove the excess polymer, the solution was then ultracentrifuged at 55,000 rpm (367,000g) for 5 h. Over 90% of the dispersed SWNTs precipitated at the bottom of the centrifuge tube, accumulating into a little but visible black “mat”. The “mat” was carefully collected, washed three times with 2–3 mL of fresh toluene, and further subjected to a short sonication step in 2 mL toluene (20 min, 15 W), yielding the semiconductor-enriched SWNT dispersion with little excess polymer.

The absorption spectra of the as-dispersed sSWNTs and the one after enrichment are shown in Figure 1a. The absorption spectrum of the as-dispersed SWNTs (solid red line) shows almost no background intensity, that indicates the minimum presence of bundles or metallic species. The peaks at wavelength higher than 900 nm are the E11 transition of 5 different sSWNT species corresponding to (7,5), (7,6), (8,6), (8,7), and (9,7), with the most dominant chirality of (7,5). The absorbance below 400 nm, and 7.8 μg/mL for the enriched one. No optical transitions from metallic SWNT in the spectral range of 420–600 nm were observed. The absorbance below the 400 nm spectral range (λmax = 380 nm) was also significantly lowered from a saturated value to be as low as O.D. = 0.44, which corresponds to the estimated concentration of PFO between 2.92 and 7.00 μg/mL (see the Supporting Information). This indicates that the method we applied is highly efficient in removing excess PFO polymer from the dispersion. It should be noted that, to our knowledge, the value of the residual absorbance of PFO in the sSWNT dispersion has never been reported, despite its importance in determining the remaining excess polymer. We assumed that the remaining polymer chains in our enriched solution were only those that wrapped around the sSWNT surface.

In comparison to other recent efforts to remove the excess polymer from PFO dispersed sSWNTs, our method is the most cost efficient with minimal usage of solvents, and is yet effective. Vijayaraghavan et al. reported the realization of carbon nanotube device arrays by using PFO/toluene dispersed HiPco SWNTs with a rinsing process performed after film deposition. The electronic transport characteristics indicated that there was still a significant amount of remaining polymer. Bindl et al. presented a method with multiple centrifugation steps to get rid of the excess polymer, analogous to our technique. Our procedure has the advantage of being much less time-consuming, due to shorter centrifugation times (less than 8 h, in comparison to >24 h in the previously reported methods) as well as avoiding any iterative redispersion and repelletization. Moreover, our result showed less background indicating lower amounts of nanotube bundles.

Subsequently, sSWNT FETs were fabricated by simply drop-casting the sSWNT dispersion in toluene on pre-patterned bottom-gate/bottom-contact substrates. Prior to the drop-casting of the nanotubes, the SiO2 surface was modified with a self-assembled monolayer of 3-aminopropyltriethoxysilane (APTES) to induce adhesion of sSWNTs. The drop-casting of the sSWNT dispersion was performed on a hotplate at 70 °C, to minimize the coffee-stain effect. For each device, we used 10 droplets of sSWNT dispersion with 0.5 μL/droplet and 10 s interval between depositions. Figure 1c shows the atomic force microscopy image of the deposited sSWNT network in the device channel.

The FET measurements were performed in a nitrogen-filled glovebox at room temperature in the dark and the devices were never exposed to air or moisture during and after the fabrication process. Figure 2a shows the I_D−V_DS output characteristics of the FET with 20 μm channel length and 10 mm channel width. Ambipolar characteristics of the device are observed with obvious saturation behavior in both hole and electron accumulation regimes. The I_D for p-channel operations shows a nearly linear increase in the V_DS range of 6 to –2 V. This suggests that ohmic contacts for hole injection can be achieved between the Au electrodes and the sSWNT network. On the other hand, the devices only exhibit weak nonlinear Schottky behavior for electron injection. The transfer characteristics shown in Figure 2b indicate an “on/off” ratio as high as 10^6 and 10^5 for electrons and holes, respectively. The off current is of the order of 10^−1 nA at a source drain bias, V_DS = –5 V, and the respective “on” current can reach 26 mA. The values of the on/off ratio are the
highest reported for ambipolar FETs of a sSWNT network. Both the nearly ohmic characteristics as well as the very high on/off ratios indicate the very high quality of the sSWNT dispersion and minimal influence of the residual polymer chains in the film.

The values of charge carrier mobilities were calculated from the $I_D$–$V_G$ transfer curve at the linear regime ($V_{DS} = \pm 5 \text{ V}$) by using the following formula

$$
\mu = \frac{L_C}{W_C V_{DS} C} \frac{\partial I_D}{\partial V_G}
$$

where $L_C$ and $W_C$ are the channel length and width, respectively, defined by the pattern of the electrodes. $C$ is the capacitance, in which we use the parallel plate model, $C = \varepsilon \varepsilon_0 / t_{OX} = 15 \text{ nF/cm}^2$ with $\varepsilon$ is the dielectric constant and $t_{OX}$ is the thickness of the SiO$_2$. By applying the previous formula, we obtained mobility values of 0.08 cm$^2$/V·s and 2.86 cm$^2$/V·s for holes and electrons, respectively. To the best of our knowledge, these values, particularly the electron mobility, are among the highest reported for ambipolar devices made with solution processed bottom-gated random network sSWNT devices.

The parallel plate model can overestimate the capacitance of the carbon nanotube networks, thus underestimate the value of the extracted carrier mobilities. There is a more rigorous approach to calculate the capacitance for the mobility extraction, which considers the percolation of the sSWNT network.$^{[11a,16]}$

This is expressed by the formula

$$
C = C_Q^{-1} + \frac{1}{2 \pi \varepsilon_0} \ln \left( \frac{\Delta_0 \sinh(2\pi t_{OX}/\Delta_0)}{R} \right) \left( \frac{\partial}{\partial \varepsilon} \right)_0^{-1}
$$

where $C_Q$ is the quantum capacitance of carbon nanotubes ($C_Q = e g = 2.3, g$ is the electron density of states in the nanotube$^{[17]}$; $\Delta_0^{-1}$ is the density and $R$ is the radius of the nanotube, respectively. In an infinite 2-dimentional nanotube percolation network, we can have $L (\pi N_j)^{1/2} = 4.236$ as the relationship between the percolation threshold ($N_j$)$^{[18]}$ and the nanotube length ($L$). From the AFM image of sparsely deposited sSWNT (Figure 1b), we determined the average length of our nanotube as 659 nm. Therefore, the percolation threshold of our film was 13/μm$^2$. Meanwhile, we calculated the density of the nanotube on a sSWNT FET channel approximating $90/\mu m^2$ from the AFM image of the device channel (Figure 1c). Therefore, the fabricated nanotube networks in our devices are above the percolation threshold. This also justifies the use of the parallel plate capacitance to extract the values of charge carrier mobility, since the capacitance of the network model approaches one of the parallel plate model in this limit. Nevertheless, we also calculated the capacitance using Equation 2 with $\Delta_0^{-1}$ of 10/μm. From the absorption spectrum shown in Figure 1a, the primary dispersed nanotube is (7,5) with a diameter (2πR) of ca. 0.82 nm. By using the more rigorous model above, we obtain a value for the capacitance of 12.02 nF/cm$^2$, which is very close to the value of the parallel plate model. Therefore, the values of the carrier mobility are 0.12 cm$^2$/V·s and 3.57 cm$^2$/V·s for holes and electrons, respectively.

Figure 3 shows the mobility and on/off ratio of holes (Figure 3a) and electron (Figure 3b) of the sSWNT FETs as function of the device channel lengths. More than 10 devices were measured for each channel length. All devices have a channel width of 10 mm, while the lengths were varied (5, 10, and 20 μm). The mobility values for both hole and electron remain almost constant (within the same order) with the increase of the channel length. This indicates the existence of bulk mobility in these devices. The presence of metallic nanotubes and percolation effects usually cause severe length dependent mobility in network sSWNT FETs, where the mobility value increases by channel length.$^{[19]}$ In contrast, our results show that there is no dependency, implying the absence of metallic species in our nanotube films. Moreover, the on/off ratios remain constant with $10^4$ for electron and $10^5$ for holes, even for the bottom-contact devices with very short channel length (5 μm). To the best of our knowledge, very high on/off ratio in random network nanotube devices with a short channel length are unprecedented so far. The results of the length dependent electronic transport unambiguously confirm the high purity of the sSWNT dispersion being free of metallic tubes and only containing minimal amounts of residual polymer. The results are also in good agreement with the optical measurements (see the Supporting Information, Figure S4).
In the past years, many authors have pointed out that the exposure of sSWNTs to oxygen and water moisture can induce hole doping and electron trapping. To study this effect in regard to random network enriched sSWNT FETs, we compared devices that were stored for a prolonged time in a N₂ glovebox atmosphere with devices that were exposed to air for a short period. We performed this comparison also due to the fact that the performance benchmark for sSWNT FETs is always provided for hole transport characteristics after oxygen exposure. This can be due to short time exposure during transfer of the device, or because the device measurements are performed in air. To remove the PFO from the film, in order to avoid any influence of residual polymer during the oxygen exposure experiment, the sSWNT devices were baked inside two different vacuum ovens (400 °C, 4 h, P < 10⁻¹ mbar). For the air-exposed samples, we used an oven outside the glovebox, with a total air (18 °C, Rₜ ≈ 80%) exposure time of 30 min. For the samples without air exposure we used a vacuum oven connected to the glove box. The final device measurements were performed inside a N₂-filled glovebox for both set of samples.

Figure 4a,b show the comparison between the transfer characteristics of p-channel and n-channel operation before and after air exposure, respectively. The ambipolar characteristics are still present also for the device that was exposed to the air for relatively short time (30 min). Nevertheless, the threshold voltage...
voltage for electron accumulation increases by 23 V and the electron mobility decreases to 2.52 \times 10^{-3} \text{ cm}^2\text{V}^{-1}\text{s}^{-1} from 2.86 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}. The threshold voltage \( (V_{\text{th}}) \) for hole accumulation is only decreased by 4 V. Our results are consistent with the ones reported by Aguirre et al.,\textsuperscript{[21]} which demonstrated that the oxygen/water redox couple could electrochemically induce electron transfer from sSWNTs and suppress electron conduction. This contributes to the deep electron traps that shift the \( V_{\text{th}} \) for electrons. The removal of the oxygen/water redox couple cannot be done by simply storing the device inside the glove box or under vacuum.\textsuperscript{[21]} On the other hand, the \( V_{\text{th}} \) shift of holes occurred due to the oxygen doping of the nanotube that concurrently affects the value of the \( V_{\text{th}} \) shift of electrons. Therefore, we estimate that the number of trapped electrons by the oxygen/water redox couple is \( 1.8 \times 10^{11} \text{cm}^{-2} \) from the residual \( V_{\text{th}} \) shift of 19 V.\textsuperscript{[21]} It is important to note that the devices that were annealed in a vacuum oven connected to the glovebox did not undergo any changes in their ambipolar characteristics.

The linear parallel-plate-model mobility for holes increases to 2.66 cm\(^2\)/V·s (compared with the original value prior to air exposure of 0.08 cm\(^2\)/V·s) in the devices that were exposed to air. Moreover, the value of hole on/off ratio after air exposure increased to \(10^6\), due to the increase of the “on” current. From Figure 4c, one can see that the on/off ratio and the value of hole mobility remain almost constant for all measured devices with different channel lengths even after air exposure. This demonstrates both the absence of metallic tubes and the bulk nature of hole transport in the annealed devices, and that oxygen exposure only changes the polarity of the transistor to favor more hole dominant transport.

Figure 5 shows the comparison of the performance of our ambipolar FETs with the other reported nanotube network devices fabricated on SiO\(_2\)/Si gates. The plot allows comparison of the values of hole mobility and on/off ratios, since most of the previous examples reported these two values,\textsuperscript{[8,23,27b-28]} but the devices rarely showed ambipolar transport characteristics.\textsuperscript{[1,5]} From Figure 5, it is clear that our sSWNT FETs showed excellent performance combining high on/off ratios and high values of hole mobility, among the highest for short channel, random-network, solution-processed nanotube films. Moreover, the on/off ratio for the electron transport is the highest ever reported for solution processed sSWNTs FETs.

In terms of hole mobilities, the values we obtained are still lower than those of CVD-grown nanotube networks\textsuperscript{[29]} and some of the solution dispersed nanotubes, but those were achieved with alignment processes during the deposition.\textsuperscript{[16,23-26]} Nevertheless, the on/off ratios presented herein are far better than for those devices. For the CVD-grown sSWNTs, the on/off ratios are usually in the range of \(10^{-10}\), because of the high portion of metallic tubes.\textsuperscript{[21]} A similar reason undervalues the high on/off ratio reported by Sun et al., which occurs only for very long channel devices.\textsuperscript{[25]} The high mobility demonstrated by Miyata et al. is due to the alignment process during deposition, in addition to the length sorting of the nanotubes.\textsuperscript{[24,25]} The higher on/off ratios obtained in our experiments indicate the successful enrichment process obtained using PFO. From this comparison, it becomes obvious that both our enrichment and purification techniques for sSWNT are the most efficient ways to produce high performance solution processable SWNTs devices. The utilization of this nanotube ink in combination with optimized device fabrication processes (e.g., nanotube alignment,\textsuperscript{[19]} length sorting,\textsuperscript{[23]} advanced gating,\textsuperscript{[27]} passivation,\textsuperscript{[33,27b]} ink-jet printing,\textsuperscript{[8,27b]} etc.) will be the subject for future research.

In conclusion, we reported a fast, scalable, efficient, and cost-effective method to obtain enriched sSWNT solutions from CoMoCAT nanotubes. In particular we developed an effective technique to remove excess conjugated polymer, which acts as a selective dispersing agent. By employing this sSWNT dispersion, we successfully realized ambipolar FET fabrication and operation of networks of sSWNT with a record value of electron mobility (3 cm\(^2\)/V·s). The hole mobility of the devices also reached high values after exposure of the devices to air. The very high values of the on/off ratios for hole and electron accumulation (10\(^6\)) showed that the deposited sSWNT films have no metallic content, placing these devices amongst the best ever obtained with a solution-processed random-network sSWNT configuration. The purity of the resulting nanotube ink as well as the fast and efficient method to obtain it will make the scaling up of the fabrication of high performance sSWNT FETs more feasible, in a close competition with the other nanocarbon technologies (e.g., graphene).

**Experimental Section**

CoMoCAT SWNTs and poly(9,9-di-n-octylfluorenyl-2,7-diyl) (PFO) (\(M_n = 58,200\text{ g/mol}\) by GPC) were obtained from Southwest Nanotech and Sigma-Aldrich, respectively. The solution of sSWNT and polymer was...
ultrasonicated (in Misonic 3000 water bath sonicator) at 90 W power. We used a Beckman Coulter (Optima XE-90, SW 55 Ti) ultracentrifuge for both processes to disperse the nanotube and to remove the excess polymer. The absorption spectra of the dispersion were recorded using a UV-vis-NIR spectrophotometer (Perkin-Elmer, Lambda 900) with a 1-cm-thick cuvette.

A thermally grown SiO$_2$ layer (230 nm thickness), on heavily doped Si wafer, served as the gate dielectric for the FETs. The source and drain electrodes consisted of 30 nm Au on top of a 10 nm Ti adhesive layer, Si wafer, served as the gate dielectric for the FETs. The source and drain

Supporting Information

Supporting Information is available from the Wiley Online Library or www.MaterialsViews.com

Acknowledgements

S.Z.B. and J.G. contributed equally toward this manuscript. The authors acknowledge Technologiestichting STW and NanoSci-ERA (a consortium of national funding organizations within European Research Area) for the funding of the project Nano-Hybrids for Photonic Devices (NaPhoD). We thank F. van der Horst, A. Kamp, R. Gooijarts and J. Baas for the technical support.

Received: July 4, 2012
Revised: August 20, 2012
Published online: September 24, 2012