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Bilayer h-BN barriers for tunneling contacts in fully-encapsulated monolayer MoSe₂ field-effect transistors

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Abstract
The performance of electronic and spintronic devices based on two-dimensional semiconductors (2D SC) is largely dependent on the quality and resistance of the metal/SC electrical contacts, as well as preservation of the intrinsic properties of the SC channel. Direct metal/SC interaction results in highly resistive contacts due to formation of large Schottky barriers and considerably affects the properties of the 2D SC. In this work, we address these two important issues in monolayer MoSe₂ field-effect transistors (FETs). We encapsulate the MoSe₂ channel with hexagonal boron nitride (h-BN), using bilayer h-BN at the metal/SC interface. The bilayer h-BN eliminates the metal/MoSe₂ chemical interactions, preserves the electrical properties of MoSe₂ and reduces the contact resistances by prevention of Fermi-level pinning. We investigate electrical transport in the monolayer MoSe₂ FETs that yields close to intrinsic electron mobilities (≈ 26 cm² V⁻¹ s⁻¹) even at room temperature. Moreover, we experimentally study the charge transport through metal/h-BN/MoSe₂ tunnel contacts and we explicitly show that the dielectric bilayer of h-BN provides highly efficient gating (tuning the Fermi energy) of the MoSe₂ channel at the contact regions even with small biases. Also we provide a theoretical model that allows to understand and reproduce the experimental I–V characteristics of the contacts. These observations give an insight into the electrical behavior of the metal/h-BN/2D SC heterostructure and introduce bilayer h-BN as a suitable choice for high quality tunneling contacts that allows for low energy charge and spin transport.

1. Introduction

Atomically thin transition metal dichalcogenides (TMDs) are among the most promising materials for nano-electronics. In particular, single-layer TMDs can be used as a high-mobility semiconductor channel in field effect transistors (FETs), yielding significant on/off current ratios (I_{on}/I_{off} > 10⁶) [1] and reduced power dissipation [2]. In recent years, the coupled spin-valley physics in TMDs [3] has also attracted broad attention, since it provides a new opportunity for (opto-) spin-valleytronic applications [4, 5].

A major challenge for efficient charge and spin transport in TMD-based FETs is to achieve high quality, low resistive electrical contacts at the source and drain electrodes [6–14]. Direct metal/TMD interaction causes Fermi-level pinning and formation of large Schottky barriers and affects the electrical properties of the 2D TMD layer [15, 16]. Also, the atomic thickness of the 2D channel falls below the width of the charge depletion region at the metal/TMD contact. These facts lead to highly resistive electrical contacts that significantly limits the charge injection/detection efficiency in these FETs.

An effective strategy to address these issues is to use an insertion layer in between the TMD and metal, such as MgO, TiO₂, Ta₂O₅ and h-BN [7, 9, 10, 11–14]. Among them, h-BN is more promising for 2D contacts with TMDs since deposition of other mentioned materials on the dangling bond-free surface of TMD can lead to formation of isolated atomic islands and therefore poor quality of the contacts.

Further, it was shown that bilayer h-BN tunnel barriers are highly efficient for spin injection [17]. This is not the case for monolayer h-BN, since it provides the SC channel with ohmic contacts [9] due to chemisorption of the h-BN to the metal (e.g. Ti and Co) [18] which might cause conductivity mismatch.
problem that considerably restrains the spin injection efficiency [19].

To our knowledge, a detailed description of the electrical behavior of metal/bilayer h-BN contacts for monolayer MoSe$_2$ FETs is yet to be provided. In this work, we use an exfoliated bilayer h-BN as a tunnel barrier between the MoSe$_2$ channel and the Ti electrodes that disrupts the metal/TMD chemical interactions [20, 21]. Also, as recently reported [18], the chemisorption of h-BN to Ti, lowers the Ti workfunction by 0.78 eV, improving its band alignment with the MoSe$_2$ conduction band.

Using a theoretical model for the electrical behavior of the metal/h-BN/TMD contacts, we are able to describe and reproduce the experimental observations in the three-terminal $I$–$V$ characteristics. In particular, we show that the strong out-of-plane electric fields formed at the contact interface can considerably modulate the doping of the underlying channel.

We also evaluate the electrical performance of the monolayer MoSe$_2$. The charge transport studies to date on monolayer MoSe$_2$ FETs are limited to two-terminal measurements on non-encapsulated channels [22, 23]. However, in this work, the use of metal/h-BN electrodes, instead of direct metal/TMD contacts, allows for full h-BN encapsulation of the MoSe$_2$ channel that reduces the effect of (impurity dependent) Coulomb and roughness scatterings [24, 25] and prevents degradation of the MoSe$_2$ crystal [26]. As further discussed below, the FETs are fabricated using a contact geometry that reduces the interaction between the electrodes and the channel. Thus, we can expect our four-terminal measurements to reflect the intrinsic behavior of the monolayer MoSe$_2$ at room temperature.

Figure 1 shows a sketch and an optical microscope image of the studied FETs. In these devices the monolayer MoSe$_2$ channel is encapsulated between bilayer and bulk h-BN as the top and bottom flakes, respectively. The h-BN/MoSe$_2$/h-BN heterostructure is stacked on a SiO$_2$(300 nm)/doped Si substrate, using a dry pick up technique [27] which provides polymer-free interfaces. The Ti (5 nm)/Au (75 nm) electrodes are fabricated by e-beam lithography (EBL), followed by e-beam evaporation of the metals at UHV (see methods for details). The choice of Ti for the electrodes in these devices is due to its low work-function (4.33 eV) which closely matches the electron affinity of a monolayer MoSe$_2$ ($E_C = 3.99$ eV) [28].

Our sample contains two devices that are numbered in the optical image of figure 1(b). Device 1 and 2 address the separate 1L-MoSe$_2$ flakes which are covered with 2- and 5-layer h-BN, respectively. A homogeneous bulk h-BN flake is used as bottom layer in both devices and the doped Si layer is used as a back-gate electrode.

2. Two-terminal measurements

We start with evaluation of the FET performance by two-terminal (2T) measurements. All the measurements are done at room temperature and in vacuum ($<10^{-4}$ mbar). As shown in the device sketches of figure 2, the SC channel is connected to the source and drain electrodes through the top h-BN layers and is separated from the gate electrode by the bulk h-BN and SiO$_2$. The source electrode is grounded and a voltage ($V_{sd}$) is applied on the drain electrode, while measuring the source-drain current ($I_{sd}$). We measure $I_{sd}$ as a function of gate voltage ($V_{g}$) (transfer curve) at different $V_{sd}$ for the both devices of 1 and 2 (figures 2(a) and (b)) that shows gate-modulation of channel conductivity by tuning the density of charge carriers in the MoSe$_2$ channel.

The transfer curves show an almost hysteresis-free charge transport. The $I_{sd}$ increases for $V_{g}$ larger than the threshold voltage ($V_{th}$) as the Fermi energy approaches the conduction band minimum ($E_C$). Comparison of the 2T measurements performed in device 1 and 2 shows that the 2T current injected through 2L h-BN is much larger than the current injected through 5L h-BN. From this 2T current ratio and square resistance of the channel (measured in four-terminal geometry, see next section), we estimate that the resistance of the contacts with 5L h-BN is 4 orders of magnitude larger than the ones with 2L h-BN. This observation is consistent with the fact that the increase in the number of h-BN layers reduces the charge tunneling efficiency into the channel [29] but does not show considerable improvement in lowering the Schottky barrier height [14].

The 2T source-drain $I$–$V$ measurements (figures 2(c) and (d)), show the non-linear, almost symmetric behavior, indicating the dominance of the contacts in the 2T charge transport (otherwise we would expect linear $I$–$V$ characteristics). In the SD $I$–$V$ curves of device 2 (figure 2(d)), we observe that $I_{sd}$ starts to saturate at large $V_{sd}$. This saturation can be attributed to the ‘pinch-off’ region in the SC channel, because the applied $V_{sd}$ acts as a gate and can create a depletion region in the channel close to the drain contact [30]. The saturation of the current at such low SD biases shows that these contacts can considerably gate the channel. The $V_{sd}$ at which the saturation of current happens, depends on the density of charge carriers in the channel. The higher $V_{sd}$ induces a larger density of charge carriers and therefore charge depletion close to the contacts happens at larger $V_{sd}$.

Therefore, the electrical performance of a two-terminal TMD-based FET is highly dependent on the electrical response of the contacts. When the metal comes in vicinity of the n-type 2D SC, the SC gets charge-depleted [15]. In figure 2(e), we show the schematic drawings of the metal/h-BN/SC interface, where the black gradient illustrates the depleted region that is gradually disappearing in the lateral direction of the SC along the channel. The corresponding band diagram shows the only energy level available for the states in the 2D SC channel beneath the contact (region A) which reaches the less depleted region close to the
edge of the contact (region B) and laterally expands into the SC where there will be no charge depletion and the SC bands preserve their intrinsic energy levels (region C) [15]. In this situation the charge carriers that are injected far from the edge of the contact, firstly encounter region A before reaching region B. The low density of charge carriers at these depleted regions makes the contact areas highly resistive in comparison with the channel. Therefore, in the 2T measurements, modulation of the $I_{sd}$ as a function of $V_{sd}$ and $V_{th}$ is considerably affected by the contact regions that have the main contribution in the voltage drop in the 2T circuit. In the band diagram of figure 2(e), we also show the tunnel barrier formed by the van der Waals (vdW) gap in addition to the h-BN layer. This gap might not be present in the direct metal/TMD interface, since the interaction can be accompanied with covalent bonding (strong hybridization) [31].

We also compare the performance of the 2T FETs, fabricated with encapsulated and non-encapsulated 1L-MoSe$_2$ channels (see supplementary information (SI) (stacks.iop.org/TDM/6/015002/mmedia), section 3). We observe that the BN-encapsulation of the channel considerably improves the 2T electrical transport in the 1L-MoSe$_2$. This is because TMDs are highly sensitive to environmental adsorbates or scattering centers [32]. This fact, in addition to the formation of large SB at the direct metal/TMD interface leads to one order of magnitude lower 2T conductivity and mobility in the non-encapsulated MoSe$_2$ channel. Also there is a large hysteresis in the electrical measurements on non-encapsulated MoSe$_2$ FET (about 29 V), while the BN-encapsulated sample shows almost no-hysteresis for the same gate bias sweeping rate.

3. Four-terminal measurements

In order to investigate the intrinsic electrical properties of the monolayer MoSe$_2$ channel, we perform four-terminal (4T) measurements. As shown in the device sketches of figure 3, we apply the $V_{sd}$ to the outer electrodes and measure $I_{sd}$, while probing the voltage drop across the inner electrodes ($V_{2T}$). First, we perform these measurements with the electrodes that are crossing the full width of the channel (crossing electrodes). Figure 3(a) shows the dependence of channel conductivity ($\sigma = I_{sd} / V_{sd} W_{ch}^{-1}$) on $V_{th}$. This n-type behaviour indicates that the Fermi level of the SC is closer to its conduction band. Figure 3(c) shows the linear dependence of $V_{4T}$ on $I_{sd}$. From the slope of the SD $I$–$V$ curves, we extract the square resistance ($R_{sq}$) of the channel at different $V_{th}$ ($R_{sq} = 0.5 \, \text{M}\Omega$ to $1.5 \, \text{M}\Omega$ for the range of $V_{th} = 60 \, \text{V}$ to $45 \, \text{V}$).

However, the depleted regions of the contact areas (shown in figure 2(e)) can contribute to the 4T measurements, performed by using the crossing contacts. In order to avoid the effect of the depleted regions and address the intrinsic electrical behavior of the channel material, we perform the 4T measurements using side contacts (the electrodes that are partially covering the channel). The minimized overlap of the side contacts with the MoSe$_2$ flake diminishes screening of the gate-induced charges in the channel. Figure 3(b) shows the measurement geometry and gate dependency of the channel conductivity. The dependence of $V_{4T}$ as a function of $I_{sd}$ for different $V_{th}$ is shown in figure 3(d). The considerable difference between the results of these two 4T measurement geometries (figures 3(a) and (b)) makes it clear that the role of the mentioned depleted regions underneath the contacts should not be overlooked. The conductivity of the channel, derived from the 4T measurements with the side contacts, clearly shows linear behavior for $V_{th} > V_{ch} = 20 \, \text{V}$, while the conductivity measurements with the crossing contacts are behaving more similar to the 2T measurements (figure 2(a)) that show gradual increase versus $V_{th}$ and hardly reaches the linear regime. By the linear fit to the conductivity curve for $V_{th} > V_{th}$ (in figure 3(b)), we extract the electron field-effect mobility of $\mu_{FET} \approx 26 \, \text{cm}^2 \, \text{V}^{-1} \, \text{s}^{-1}$, considering the effective width of the channel since it is partially covered by the side contacts.
The extracted $\mu_{\text{FE}}$ in the 1L-MoSe$_2$ channel is close to the intrinsic value predicted by theoretical studies [33]. This indicates that the electrical properties of the channel are well preserved in our sample. The BN-encapsulation of the channel reduces the Coulomb scatterings caused by charged impurities at the interfaces or on the channel surface (e.g. fabrication residues) and also eliminates roughness scatterings originating from the Si substrate [34, 35] that could highly affect the charge carrier mobility [36, 37]. From the linear $I$–$V$ curves measured with the side contacts, we extract the values of $R_{\text{sq}} = 130 \, \text{k}\Omega$ to $190 \, \text{k}\Omega$ for the range of $V_\text{g} = 60 \, \text{V}$ to $45 \, \text{V}$. The channel resistances is about 1 order of magnitude smaller than the ones measured with the crossing contacts. The derived $R_{\text{sq}}$ and $\mu_{\text{FE}}$ are consistent with the results of the same measurements done

**Figure 2.** Two-terminal electrical measurements. (a) and (b) Source-drain (SD) current ($I_{\text{sd}}$) as a function of gate voltage ($V_\text{g}$) measured at different SD biases ($V_{\text{sd}}$) on (a) 1L-MoSe$_2$ (channel length of $L_{\text{ch}} = 0.93 \, \mu\text{m}$ and channel width of $W_{\text{ch}} = 2.8 \, \mu\text{m}$), covered with 2 layers of h-BN and (b) 1L-MoSe$_2$ (with $L_{\text{ch}} = 2.54 \, \mu\text{m}$ and $W_{\text{ch}} = 1.6 \, \mu\text{m}$), covered with 5 layers of h-BN. Sketches of the device and measurement geometry are shown in the insets. (c) and (d) SD $I$–$V$ characteristics at different $V_{\text{g}}$ for the FETs with 2L (c) and 5L (d) h-BN. (e) Schematic drawing and the corresponding band diagram of a Ti/h-BN/MoSe$_2$ heterostructure. The black gradient below the contact illustrates the highly depleted region (A) underneath the contact that extends laterally in the 2D SC through region (B) until it reaches the non-depleted region (C).
with the other pairs of side contacts (see figure 1(b)).
In contrast, the 4T measurements performed with the crossing electrodes vary for different contacts along the channel because of the inhomogeneity of the contact regions (can be due to the presence of bubbles at the 2D interfaces or variation of the contact area). This observation is another evidence for contribution of the depletion regions from the crossing contacts in the latter case.

4. Modeling of metal/h-BN/TMD tunnel contact and three-terminal measurements

As shown, the performance of the TMD-based FETs is largely affected by the contact regions. We model the electrical behavior of the contacts using the band diagram of the metal/h-BN/TMD heterostructure shown in figure 4. The system can be conceptually understood as a parallel-plate capacitor with a small leakage current (tunneling through the h-BN). When the applied voltage between the metal and MoSe$_2$ is zero (figure 4(a)), the Fermi levels at the metal and MoSe$_2$ are the same, $E_{F,M} = E_{F,S}$. The band alignment between h-BN and MoSe$_2$ will be determined by the difference between their electron affinities ($\chi_{MoSe_2} - \chi_{BN}$). At the metal side, the band alignment will be given by the difference between the electron affinity of h-BN and the work function of titanium ($\phi_{M} - \chi_{BN}$). We consider $\phi_{M}$ shifted by 0.78 eV due to the chemisorption of h-BN to Ti [18]. The position of $E_{F,S}$ with respect to $E_{C}$ is controlled by the gate voltage. Importantly, the matching of the Fermi energies between Ti and MoSe$_2$ at equilibrium requires a misalignment of the vacuum levels at the two sides of the h-BN barrier. Thus, even at zero applied bias, a nonzero electrostatic voltage drop $V_{BN}$ will appear at the h-BN layer.

$V_{ST}$ is the voltage drop across the tunnel barrier which is the difference between the Fermi energies of the metal and semiconductor. When $V_{ST} \neq 0$ (see figures 4(b)–(d)), the Fermi energies at both sides become misaligned by $E_{F,M} - E_{F,S} = qV_{ST}$ where $-q$
is the electron charge. Then, owing to the capacitive coupling between metal and MoSe$_2$, the charge density of MoSe$_2$ changes. Due to the limited density of available states, this will cause a shift in the position of the edge of the conduction band with respect to $E_{F,S}$. From the band diagrams of figure 4, one gets the relation

$$qV_{BN} - qV_{ST} = \phi_M - \chi_{MoSe_2} + (E_{F,S} - E_C). \quad (1)$$

In order to clarify the meaning of equation (1), we now consider two situations. First, let us assume that the density of states at the MoSe$_2$ near $E_{F,S}$ is large (e.g. when the Fermi energy is well inside the conduction band of MoSe$_2$, figure 4(b)). Then, when the three-terminal voltage is increased by $\Delta V_{ST}$, charge carriers can accumulate in the MoSe$_2$ layer by filling empty states without noticeably shifting $E_{F,S}$ with respect to $E_C$, $\Delta (E_{F,S} - E_C) \approx 0$ and, from equation (1), $\Delta V_{BN} \approx \Delta V_{ST}$. Thus, the shape of the h-BN tunnel barrier, as well as the energy window at which electrons can tunnel through the barrier (highlighted in yellow in figure 4(b)) are strongly dependent on $V_{ST}$. On the other hand, if the density of states at the MoSe$_2$ near the Fermi energy is very low (i.e. when $E_{F,S}$ is inside the bandgap, figure 4(c)), charge can only be accumulated (or removed) by largely shifting $E_{F,S}$ with respect to $E_C$. In this case, the shape of the h-BN barrier and the tunneling energy window are barely affected by changing $V_{ST}$. Thus, the tunneling current through the barrier becomes saturated. (For extended discussion on the influence of the gap states see SI, section 6).

Next, we use the band diagrams discussed above to model the tunneling $I$–$V$ characteristic of the h-BN barrier. The tunneling current density, $J_{tunnel}$ is approximately given by [38]

$$J_{tunnel} = A \times \sum_{\text{all bands}} 2 \int g_{2D} \times P_{tunnel}(E) \times (f(E - E_F) - f(E - F_S - V_{ST})) dE \quad (2)$$

where we model the MoSe$_2$ density of states as that of a 2D electron gas, $g_{2D} = m^* / (\pi \hbar^2)$. $P_{tunnel}$ is the transmission probability through the h-BN barrier, $f$ is the Fermi–Dirac distribution function and the sum runs for all the spin–orbit split subbands. $A$ is a contact-dependent fitting parameter to be empirically determined. The factor 2 before the integrals accounts for the valley degeneracy. For simplicity, we approximate the trapezoidal tunnel barrier (indicated in green in the band diagrams of figure 4) by a rectangular barrier with an effective barrier height equal to the average height along the h-BN (orange line in the diagrams). We further assume that $P_{tunnel}$ is exponentially dependent on the energy of the tunneling electrons as

$$P_{tunnel}(E) = \exp \left( -\frac{B \sqrt{2m^*}}{\hbar} \times \sqrt{U_{\text{barrier}} - E} \right) \quad (3)$$

where $U_{\text{barrier}} = \chi_{MoSe_2} - \chi_{BN} + \frac{q}{2} V_{BN}$ is the tunneling barrier height and $E$ is the electron energy.
with respect to $E_C \cdot B$, similar to $A$ in equation (2), is used as a contact-dependent fitting parameter. Note that, for the case of isotropic, parabolic bands one gets $B = 1$, while we get the best fit to our experimental data considering $B = 0.3$. Figure 4(e) shows the fit (red line) to an experimental three-terminal $I$–$V$ (markers) with $V_{\text{gate}} = 40$ V. The $I$–$V$ characteristic can be divided in two different regimes, depending on whether the $E_{FS}$ is above (figures 4(a) and (b)) or below (figures 4(c) and (d)) $E_C$. For $E_{FS} > E_C$, the density of states at the MoSe$_2$ is large. Thus, the barrier height $U_{\text{barrier}} \propto V_{B3T}$ will change almost linearly with $V_{3T}$. For $E_{FS} < E_C$, however, the density of states becomes much lower. Thus, $U_{\text{barrier}}$ changes more slowly with $V_{3T}$. The transition between these two regimes creates a kink in the $I$–$V$ curves at $V_{3T} \approx 0.3$ V. In the ideal case where the density of gap states $g_{\text{gap}}$ is zero, $U_{\text{barrier}}$ remains constant, and the tunneling current becomes completely saturated (green, dashed line in figure 4(e)). In order to reproduce the experimental $I$–$V$ we need to consider a nonzero density of gap states $g_{\text{gap}}$. For simplicity, we consider $g_{\text{gap}}$ to be energy-independent (see SI, section 6) $g_{\text{gap}} = 1.8\% g_{2D}$, which leads to a nonideal current saturation (red, solid line). It is worth noting that in reality $g_{\text{gap}}$ is expected to be markedly energy-dependent, with a large density of gap states concentrated at energies near the conduction and valence band edges (further discussed in SI). An additional fitting parameter is the threshold gate voltage $V_{\text{th}}$, which shifts the position of $E_{FS}$ with respect to $E_C$ and thus determines the value of $V_{3T}$ at which $E_{FS} = E_C$. From the fit to the experimental $I$–$V$ we get $V_{\text{th}} = 90$ V, well above the values extracted from four-terminal transfer characteristics. This indicates that the MoSe$_2$ flake is depleted by the proximity of the metallic contacts, as also discussed in section 3.

Figure 4(f) shows the experimental three-terminal $I$–$V$ characteristics (markers) for different gate voltages $V_{\text{g}}$ ranging from 30 to 60 V. The nonlinear $3T$ $I$–$V$ curves show the tunneling diode behavior of the contact with forward negative bias. We fit the full set of $I$–$V$ curves to equation (2) (solid lines), using $g_{\text{gap}}$ as the only free parameter, while keeping $A, B$ and $V_{\text{th}}$ fixed. The full set of curves is well reproduced by the model considering a small density of gap states. The inset panel in figure 4(f) shows the values of $g_{\text{gap}}$ extracted from the fits, increasing from $g_{\text{gap}} = 0.8\% g_{2D}$ at $V_{g} = 30$ V to $g_{\text{gap}} = 6\% g_{2D}$ at $V_{g} = 60$ V. This increase of $g_{\text{gap}}$ for large gate voltages suggests that the density of gap states is indeed energy-dependent (rather than constant, as considered in the model, see SI), with a larger concentration of states.
for energies close to the conduction band edge. In order to check the consistency of the values of \( g_{\text{gap}} \) extracted from the fits, we estimate its lower limit from the four-terminal transfer curves (with a \( V_g \) sweep of \( \pm 60 \) V and \( V_{th} = 20 \) V). Assuming that switching from conduction to valence band requires at least a \( V_{\text{gate}} \) shift of 80 V, we get \( g_{\text{gap}} \approx 1.5\% \cdot g_{\text{2D}} \), compatible with the values extracted from the model.

Figure 4(g) shows the contact resistance-area product \( (R_C \cdot A) \) of the Ti/h-BN/TMD tunneling diode, as a function of \( V_g \). The contact resistance \( (R_C) \) is defined as the resistance of the metal/SC interface plus the resistance of the SC channel beneath the contact [39]. We observe strong modulation of \( R_C \cdot A \) as a function of \( V_g \) for the range of \( V_g < 40 \) V at high \( V_{sd} \) which is not as large for low \( V_{sd} \). Consistent with our explanation regarding the reverse (positive) bias of the Ti/h-BN/TMD tunneling diode, the positive \( V_{sd} \) lowers the \( E_F \) towards the SC bandgap, where changing the gate voltage can considerably tune the density of the sates. However, at very low \( V_{sd} \), the \( E_F \) is still positioned in or close to the conduction band. Therefore the change in the density of states and so the contact resistance as a function of \( V_g \) is not as dramatic. The strong gate and bias dependence of the contact resistance and the tunneling current confirms efficient gating of the TMD channel beneath the contacts.

This gating effect of the bias applied on the contact is further evidenced by comparison of the two three-terminal (3T) measurement geometries, shown in figure 5(a). We also illustrate the resistor network of the metal/h-BN/TMD interface (in figure 5(b)), consisting of metal/h-BN/TMD contact resistance (in black), TMD channel beneath the contact (in blue) and TMD channel in between the two contacts (in green). In the 3T geometries, \( V_{3T} \) is the summation of the voltage drop on contact 2 (voltage on node N1 of figure 5(b)) and the voltage drop across the channel in between contact 1 and 2. The \( V_{3T} \) in this geometry measures the voltage of node N2 (of contact 2).

Measurement of \( V_{3T}^1 \) and \( V_{3T}^2 \) as a function of \( I_{sd} \) is compared in figure 5(c). As discussed, the channel resistance is order(s) of magnitude smaller than contact resistances. Therefore, the \( I-V \) characteristics from figure 5(c) are dominated by the voltage drop on the contact (C2) in both geometries. However, for almost all of the crossing contacts we observe distinct behavior of \( V_{3T}^1 \) and \( V_{3T}^2 \) for the positive range of the 3T voltages. Further, the deviation exactly occurs at the kink mentioned in the 3T \( I-V \) of figure 4(e), where the Fermi energy is below \( E_C \). Such charge depletion underneath the contact makes the in-plane resistivity (blue resistors of figure 5(b)) to be comparable to the out-of-plane resistivity (black resistors). In this case the charge current that flows through the left side of the contact (node N1) is higher than that of the right side (node N2) and therefore the \( V_{3T}^2 \) saturates to values lower than \( V_{3T}^1 \).

Figure 5(d) shows the shift of \( E_F \) with respect to \( E_C \) as a function of the three-terminal voltage for \( V_g = V_{th} = 50 \) V = 40 V. The kink in this diagram corresponds to the point at which the conduction band becomes fully depleted, closely matching the voltage at which the \( I-V \) curves from panel c start to diverge. Note that, as discussed above, even relatively small changes in \( V_{3T} \) can strongly modulate the doping of the channel. In particular, we observe that, even if the SiO2 back gate is 50 V below the threshold voltage, a \( V_{3T} \) as small as \( -0.2 \) V is sufficient for \( E_F \) to reach the edge of the conduction band. Such considerable gating effect even at small biases due to the capacitor formed at the metal/bilayer h-BN contacts makes this contact geometry favorable for source-gated transistors [40, 41].

5. Conclusions

Our results show that metal/bilayer h-BN electrodes are very promising for low-energy transport in 2D TMD-based devices, allowing to reduce the effects of Fermi level pinning and formation of Schottky barriers. In consequence, we find that bilayer h-BN tunnel contacts outperform direct metal/SC contacts both in terms of electrical response and quality, yielding reduced hysteresis. Elimination of metal/ TMD chemical interactions by the h-BN insertion layer along with the full BN-encapsulation of the channel and the side electrode geometry, allows to better preserve the intrinsic properties of the 2D channel and reach carrier mobilities comparable to those of the pristine MoSe2 at even at room temperature. The model described here for h-BN tunnel contacts to 2D semiconductors allows to satisfactorily describe and reproduce their experimental electrical response, showing that the doping of the semiconductor channel below the contacts can be largely modified even for relatively small source-drain bias voltages.

6. Methods

Atomically thin layers of MoSe2 and h-BN are mechanically cleaved from their bulk crystals on SiO2/Si substrates, using adhesive tapes [42]. We identify the thin flakes by their optical contrast with respect to the substrate [43, 44] and verify their thicknesses by atomic force microscopy (AFM). Using dry pick-up technique [27], we pick up the bilayer h-BN flake by PC (poly(bisphenol A)carbonate) and a PDMS stamp. We use the bilayer h-BN flake on the PC layer to pick up monolayer MoSe2 by means of vdW interactions between the h-BN and MoSe2 flakes. Then, we release the picked-up flakes on top of the bulk h-BN on SiO2 (300 nm)/doped Si substrate, by melting the PC. The PC layer is dissolved in Chloroform for 5 min and the residues are removed by annealing the sample in Ar/H2 flow at 350 C for 3 h. We proceed with fabrication of electrodes on the vdW stack by e-beam lithography technique (using PMMA as the e-beam resist) and e-beam evaporation of Ti (5 nm)/Au (75 nm) at the
pressure of $10^{-6}$ mbar, followed with lift-off in Acetone at 40 °C. The UHV condition for evaporation of the electrodes is to minimize interfacial contamination and more importantly to avoid oxidation of Ti.

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