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A Robust Consensus Algorithm for Current Sharing and Voltage Regulation in DC Microgrids

Michele Cucuzzella, Sebastian Trip, Claudio De Persis, Xiaodong Cheng, Antonella Ferrara, and Arjan van der Schaft

Abstract—In this paper a novel distributed control algorithm for current sharing and voltage regulation in Direct Current (DC) microgrids is proposed. The DC microgrid is composed of several Distributed Generation units (DGUs), including Buck converters and current loads. The considered model permits an arbitrary network topology and is affected by unknown load demand and modelling uncertainties. The proposed control strategy exploits a communication network to achieve proportional current sharing using a consensus-like algorithm. Voltage regulation is achieved by constraining the system to a suitable manifold. Two robust control strategies of Sliding Mode (SM) type are developed to reach the desired manifold in a finite time. The proposed control scheme is formally analyzed, proving the achievement of proportional current sharing, while guaranteeing that the weighted average voltage of the microgrid is identical to the weighted average of the voltage references.

Index Terms—DC Microgrids, Sliding mode control, Uncertain systems, Current sharing, Voltage regulation.

I. INTRODUCTION

In the last decades, due to economic, technological and environmental aspects, the main trends in power systems focused on the modification of the traditional power generation and transmission systems towards incorporating smaller Distributed Generation units (DGUs). Moreover, the ever-increasing energy demand and the concern about the climate change have encouraged the wide diffusion of Renewable Energy Sources (RES). The so-called microgrids have been proposed as conceptual solutions to integrate different types of RES and to electrify remote areas. Microgrids are low-voltage electrical distribution networks, composed of clusters of DGUs, loads and storage systems interconnected through power lines [2].

Due to the widespread use of Alternate Current (AC) electricity in most industrial, commercial and residential applications, the recent literature on this topic mainly focused on AC microgrids [3]–[7]. However, several sources and loads (e.g. photovoltaic panels, batteries, electronic appliances and electric vehicles) can be directly connected to DC microgrids by using DC-DC converters. Indeed, several aspects make DC microgrids more efficient and reliable than AC microgrids [8]: i) lossy DC-AC and AC-DC conversion stages are reduced, ii) there is no reactive power, iii) harmonics are not present, iv) frequency synchronization is overcame, v) the skin effect is absent. Moreover, a DC microgrid can be connected to an islanded AC microgrid (even to the main grid) by a DC-AC bidirectional converter, forming a so-called hybrid microgrid [9]. Moreover, the growing need of interconnecting distant power networks (e.g. off-shore wind farms) has encouraged the use of High Voltage Direct Current (HVDC) technology [10]–[12], which is advantageous not only for long distances, but also for underwater cables, asynchronous networks and grids running at different frequencies [13]. Finally, DC microgrids are widely deployed in aircrafts and trains, and recently used in modern design for ships and large charging facilities for electric vehicles. For all these reasons, DC microgrids are attracting growing interest and receive much research attention.

A. Literature review

Two main control objectives in DC microgrids are voltage regulation and current sharing (or, equivalently, load sharing). Regulating the voltages is required to ensure a proper functioning of connected loads [14]–[17], whereas current sharing prevents the overstressing of any source. Moreover, since a microgrid can include DGUs with different generation capacity, it is often desired in practical cases that the DGUs share the total current demand proportionally to their generation capacity. In order to achieve both objectives, hierarchical control schemes are conventionally adopted [18]. Generally, the requirement of current sharing does not permit to regulate the voltage at each node towards the corresponding desired value. Then, a reasonable alternative is to satisfy the voltage requirement defined in [19], according to which the average voltage across the whole microgrid (not a specific node) should be regulated at the global voltage set point (e.g., the average of the voltage references). This kind of voltage regulation is called global voltage regulation or voltage balancing (see for instance [20]–[24] and the references therein).

In the literature, these control problems in DC microgrids have been addressed by different control approaches and
schemes, and we discuss a few of them. To compensate the voltage steady state error due to primary droop controller, a distributed secondary controller based on averaging the total current supplied by the sources is proposed in [25]. Yet, for the stability analysis, fast dynamics are neglected and only the small-signal model is considered. Distributed secondary integral control strategies that are able to achieve proportional load sharing and voltage regulation are formally analyzed in [26], neglecting inductive lines. In [19] each power converter is equipped with current and voltage regulators in order to achieve both proportional load sharing and voltage regulation. However, the achievement of voltage regulation requires the use of an observer to estimate the global average voltage, leading to more complicated controller implementations. In [24] the authors propose a consensus-based secondary controller for proportional current sharing and global voltage regulation for resistive networks. However, proportional current sharing is achieved under the restrictive assumptions that the line resistances are known and the electrical and communication graphs are identical. A consensus algorithm that guarantees power sharing in presence of ‘ZIP’ (constant impedance, constant current, constant power) loads, as well as preservation of the weighted geometric average of the source voltages is designed and formally analyzed in [27]. However, only pure resistive networks are considered and the steady state voltages strongly depend on the voltage initial conditions.

B. Main contributions

This paper proposes a novel robust control algorithm to obtain simultaneously proportional current sharing among the DGUs and a form of voltage regulation in the DC power network, where the interconnecting lines of the microgrid are assumed to be resistive-inductive. In order to achieve current sharing, a communication network is exploited where each DGU communicates in real-time the value of its generated current to its neighbouring DGUs. Adding this additional communication layer to achieve current sharing, leading to a distributed controller, has been widely adopted and studied thoroughly. In comparison to the existing results in the literature, we additionally propose the design of a manifold that couples the aforementioned objective of current sharing to the objective of voltage regulation. By doing this, the proposed control algorithm guarantees that the weighted average voltage of the microgrid is equal to the weighted average of the reference voltages, where the weights depend on the DGUs generation capacities, performing the so called global voltage regulation or voltage balancing [19], [24]. This is achieved independently of the initial voltage conditions, facilitating Plug-and-Play capabilities.

To constrain the state of the system to the designed manifold in a finite time, we propose robust controllers of Sliding Mode (SM) type [28], [29]. SM control is appreciated for its robustness property against a wide class of modelling uncertainties and external disturbances, commonly present in DC microgrids. In this paper, we first propose a Second Order Sliding Mode (SOSM) controller that determines the, possibly non-constant, switching frequency of the power converter, which might lead increased the power losses. Then, to overcome this issue, we additionally propose a third order sliding mode controller (3SM) to obtain a continuous control signal that can be used as the duty cycle of the power converter. Furthermore, the proposed control solution is robust with respect to failed communication. In fact, if the communication among the DGUs is disabled, then the voltage of each node converges in a finite time to the corresponding reference value. For the considered microgrid model, convergence to the state of current sharing and voltage regulation is theoretically analyzed, and we show that convergence is achieved globally, for any initialization of the microgrid.

C. Outline

The remainder of this paper is organized as follows. In Section II the microgrid model is presented, while in Section III the control problem is formulated. In Section IV the proposed manifold-based consensus algorithm is designed, and in Section V sliding mode control strategies are proposed to reach the desired manifold. In Section VI the stability properties of the controlled system are analyzed, while in Section VII the simulation results are illustrated and discussed. Some conclusions are gathered in Section VIII.

II. DC MICROGRID MODEL

In this work we consider a typical Buck converter-based DC microgrid of which a schematic electrical diagram is provided in Figure 1. By applying the Kirchhoff’s current (KCL) and voltage (KVL) laws, the governing dynamic equations of the i-th node (DGU) are the following:

\[
\begin{align*}
L_{ti} \dot{I}_{ti} &= -R_{ti}I_{ti} - V_{i} + u_{i} \\
C_{ti} \dot{V}_{i} &= I_{ti} - I_{Li} - \sum_{j \in \mathcal{N}_{i}} I_{ij},
\end{align*}
\]

where \(\mathcal{N}_{i}\) is the set of nodes (i.e., the DGUs) connected to the i-th DGU by distribution lines, while the control input \(u_{i}\) represents the Buck converter output voltage*. The current from DGU \(i\) to DGU \(j\) is denoted by \(I_{ij}\), and its dynamic is given by

\[
L_{ij} \dot{I}_{ij} = (V_{i} - V_{j}) - R_{ij}I_{ij}.
\]

The symbols used in (1) and (2) are described in Table I.

* Note that \(u_{i}\) in (1) can be expressed as \(\delta V_{DC,i}\), where \(\delta i\) is the duty cycle of the Buck \(i\) and \(V_{DC,i}\) is the DC voltage source provided by a generic renewable energy source or a battery at node \(i\).
The overall network is represented by a connected and undirected graph \( G = (\mathcal{V}, \mathcal{E}) \), where the nodes, \( \mathcal{V} = \{1, ..., n\} \), represent the DGUs and the edges, \( \mathcal{E} = \{1, ..., m\} \), represent the distribution lines interconnecting the DGUs. The network topology is represented by its corresponding incidence matrix \( B \in \mathbb{R}^{n \times m} \). The ends of edge \( k \) are arbitrarily labeled with a + and a −, and the entries of \( B \) are given by

\[
B_{ik} = \begin{cases} 
+1 & \text{if } i \text{ is the positive end of } k \\
-1 & \text{if } i \text{ is the negative end of } k \\
0 & \text{otherwise.}
\end{cases}
\]

Consequently, the overall microgrid system can be written compactly for all nodes \( i \in \mathcal{V} \) as

\[
\begin{align*}
L_i I_i &= -R_i I_i - V + u \\
C_i \dot{V} &= I_i + B I - I_L \\
L \dot{I} &= -B^T V - R I,
\end{align*}
\]

where \( I_i, V, I_L, u \in \mathbb{R}^n \), and \( I \in \mathbb{R}^m \). Moreover, \( C_i, L_i, R_i \in \mathbb{R}^{n \times n} \) and \( R, L \in \mathbb{R}^{m \times m} \) are positive definite diagonal matrices, e.g. \( R_i = \text{diag}(R_{i1}, ..., R_{in}) \). To permit the controller design in the next sections, the following assumption is introduced on the available information of the system:

**Assumption 1:** *(Available information)* The state variables \( I_i, V \) and \( I_L \) are locally available at the \( i \)-th DGU. The network parameters \( R_i, L_i, C_i, R, L \) and the current demand \( I_L \) are constant and known, but with known bounds.

**Remark 1:** *(Varying parameters and current demand)* We assume that the parameters and the current demand are constant, to allow for a steady state solution and to theoretically analyze the stability of the microgrid. Yet, the control strategy that we propose in the next sections is applicable even if this assumption is removed.

**Remark 2:** *(Kron reduction)* Note that in (1), the load currents are located at the PCC of each DGU (see also Figure 1). This situation is generally obtained by a Kron reduction of the original network, yielding an equivalent representation of the network [26]. It is important to realize that the network (topology) of the Kron reduced network is generally unknown and differs from the original network. It is therefore desirable that a control structure is independent of the underlying distribution network.

### III. CURRENT SHARING AND VOLTAGE BALANCING

In this section we make the considered control objectives explicit. First, we note that for a given constant control input \( \alpha \), a steady state solution \( (\overline{T}, \overline{V}, \overline{I}) \) to system (3) satisfies

\[
\overline{V} = -R \overline{T} + \alpha \\
-B \overline{T} = \overline{I}_i - I_L
\]

(4)

The second line of (4) implies that at steady state the total generated current \( \sum_i I_i \) is equal to the total current demand \( \sum_i I_L \). To improve the generation efficiency, it is generally desired that the total current demand is shared among the various DGUs proportionally to the generation capacity of their corresponding energy sources (proportional current sharing). This desire can be expressed as \( w_i I_i = w_j I_j \) for all \( i, j \in \mathcal{V} \), where \( w_i \) relates to the generation capacity of converter \( i \), and leads to the first objective concerning the desired steady state value of the generated currents \( \overline{I}_i \).

**Objective 1:** *(Proportional Current sharing)*

\[
\lim_{t \to \infty} I_i(t) = \overline{I}_i = W^{-1} I_n^T i^*_i,
\]

with \( i^*_i = \frac{I_n^T I_L}{(I_n^T W^{-1} I_n)} \in \mathbb{R}, W = \text{diag}\{w_1, ..., w_n\}, w_i > 0, \) for all \( i \in \mathcal{V} \).

Note that (5) indeed satisfies \( \sum_i I_i = \sum_i \overline{I}_i = I_n^T I_L \). From the second and third lines of (4) it follows that the corresponding steady state voltages \( \overline{V} \) satisfy \( B R^{-1} B^T \overline{V} = W^{-1} I_n i^*_i - I_L \), that prescribes the value of the required differences in voltages, \( B^T \overline{V} \), achieving proportional current sharing. This admits the freedom to shift all steady state voltages with the same constant value, since \( B^T \overline{V} = B^T (\overline{V} + a I_n) \), with \( a \in \mathbb{R} \) any scalar. To define the optimal steady state voltages, we assume that for every DGU \( i \), there exists a desired reference voltage \( V^*_i \).

**Assumption 2:** *(Desired voltages)* There exists a constant reference voltage \( V^*_i \) at the PCC, for all \( i \in \mathcal{V} \).

Often the values for \( V^*_i \) are chosen identical for all \( i \in \mathcal{V} \), and are set to the desired voltage level of the overall network. Generally, the requirement of current sharing does not permit for \( \overline{V} = V^* \), and might cause voltages deviations from the corresponding reference values. Then, a reasonable alternative is to keep the weighted average value of the PCC voltages at the steady state identical to the weighted average value of the desired reference voltages \( V^* \) (voltage balancing) [24]. Particularly, we choose the weights to be \( 1/w_i \), for all \( i \in \mathcal{V} \), such that at the converters with a relatively large generation capacity, there is a relatively small voltage deviation. It is indeed a standard practise that the sources with the largest generation capacity determine the grid voltage. Therefore, given a \( V^* \), we aim at designing a controller that, in addition to Objective 1, also guarantees voltage balancing, i.e.,

\[1The incidence matrix \( B \), satisfies \( I_n^T B = 0 \), where \( I_n \in \mathbb{R}^n \) is the vector consisting of all ones.

## TABLE I

**DESCRIPTION OF THE USED SYMBOLS**

<table>
<thead>
<tr>
<th>State variables</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>( I_{ti} )</td>
<td>Generated current</td>
</tr>
<tr>
<td>( V_i )</td>
<td>Load voltage</td>
</tr>
<tr>
<td>( I_{ij} )</td>
<td>Exchanged current</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Parameters</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>( R_{ti} )</td>
<td>Filter resistance</td>
</tr>
<tr>
<td>( L_{ti} )</td>
<td>Filter inductance</td>
</tr>
<tr>
<td>( C_{ti} )</td>
<td>Shunt capacitor</td>
</tr>
<tr>
<td>( R_{i} )</td>
<td>Line resistance</td>
</tr>
<tr>
<td>( L_{i} )</td>
<td>Line inductance</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Inputs</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>( u_i )</td>
<td>Control input</td>
</tr>
<tr>
<td>( I_{ti} )</td>
<td>Unknown current demand</td>
</tr>
</tbody>
</table>
Objective 2: (Voltage balancing)

$$\lim_{t \to \infty} I_n^T W^{-1} V(t) = I_n^T W^{-1} \overline{V} = I_n^T W^{-1} V^\star. \quad (6)$$

Remark 3: (Equal current sharing) Note that by setting in (5) and (6) the weights $w_i$, for all $i \in \mathcal{V}$, identical, the total current demand is equally shared among the DGUs and the arithmetic average of the microgrid voltage is equal to the arithmetic average of the voltage references.

By substituting (5) and (6), in (4), one can easily verify that achieving Objective 1 and Objective 2 prescribes the (optimal) steady state output voltages of the Buck converters, $u = \pi^{opt}$.

Lemma 1: (Optimal feedforward input) If system (3), at steady state, achieves Objective 1 and Objective 2, then the control input $u$ to system (3) is given by

$$\pi^{opt} = -\left( BR^{-1} B^T - \Psi \right)^{-1} (\Psi V^\star + I_L), \quad (7)$$

with

$$\Psi = (I_n + BR^{-1} B^T R_t) W^{-1} I_n I_n^T W^{-1} \overline{I}_n W^{-1} R_t W^{-1} I_n, \quad (8)$$

and $I_n \in \mathbb{R}^{n \times n}$ the identity matrix.

Proof: When Objective 1 and Objective 2 hold, the steady state of (3) necessarily satisfies

$$0 = -R_t W^{-1} I_n i^\star_t - \nabla + \pi^{opt}$$

$$0 = W^{-1} I_n i^\star_t - BR^{-1} B^T \nabla - I_L$$

$$0 = I_n^T W^{-1} \nabla - I_n^T W^{-1} V^\star,$$  \hspace{1cm} (9)

with $i^\star_t = 1^T_t L_t / (1^T_t W^{-1} I_n) \in \mathbb{R}$. A tedious, but straightforward, calculation permits to solve (9) for $\pi^{opt}$, yielding (7).

In order to determine (7), exact knowledge of almost all network parameters, as well as the current demand $I_L$, is required. Since this information is not available (see also Assumption 1), we propose in the next sections distributed controllers that, provably, achieve voltage balancing using only local measurements of $V_i$, and that achieve proportional current sharing by exchanging information on $I_{ii}$ among neighbours over a communication network. In the remainder of this section we further elaborate on the steady state voltages imposed by the control objectives.

A. Steady state voltages

First, we notice that from (5) and (9) that the steady state voltages $\overline{V}$ satisfy

$$\overline{V} = -\frac{R_t W^{-1} I_n 1^T_t I_t}{1^T_n W^{-1} I_n} + \pi^{opt}. \quad (10)$$

From (7) and (10) it is evident that the steady state values of the voltages at each node depend on the loads $I_t$ and the voltage references $V^\star$. Since $V^\star$ is free to design, it can be potentially chosen in such a way that too low or too high voltages are avoided. To help the design of $V^\star$, we show that the the steady state voltages $\overline{V}_i$, for all $i \in \mathcal{V}$, are shifted by the same quantity, when $V^\star$ is altered.

Lemma 2: (Voltage shifting property) Let Objective 1 and Objective 2 hold, and let $\overline{V}_{(1)} \in \mathbb{R}^n$ denote the steady state voltage value associated to the voltage reference $V^\star_{(1)} \in \mathbb{R}^n$. Consider the new voltage reference $V^\star_{(2)} \in \mathbb{R}^n$ and the corresponding steady state voltage value $\overline{V}_{(2)} \in \mathbb{R}^n$. Then, $\Delta \overline{V} = \overline{V}_{(2)} - \overline{V}_{(1)}$ satisfies

$$\Delta \overline{V} = I_n \frac{1^T_n W^{-1} \Delta V^\star}{1^T_n W^{-1} I_n}, \quad (11)$$

with $\Delta V^\star = V^\star_{(2)} - V^\star_{(1)}$.

Proof: When Objective 2 holds, we have

$$I_n^T W^{-1} (\overline{V}_{(2)} + \Delta \overline{V}) = I_n^T W^{-1} (V^\star_{(1)} + \Delta V^\star), \quad (12)$$

which implies $I_n^T W^{-1} \Delta \overline{V} = I_n^T W^{-1} \Delta V^\star$. Bearing in mind that the voltage differences between any node of the microgrid are prescribed by the achievement of current sharing (see the paragraph below Objective 1), we have $B^T \overline{V}_{(1)} = B^T \overline{V}_{(2)}$, implying $\Delta \overline{V} = I_n \nu$, with $\nu = I_n^T W^{-1} \Delta V^\star / I_n^T W^{-1} I_n$, i.e., all the voltages are shifted by the same quantity.

Consequently, any node $i$ in the network can lower or increase its steady state voltage $\overline{V}_i$, by adjusting its own reference $V^\star_i$. Although, the design and the analysis of a voltage reference generator is postponed to a future research, the property proven in Lemma 2 could be exploited to tune the references in order to avoid that the voltages at some nodes are lower or higher than some given thresholds.

IV. A MANIFOLD-BASED CONSENSUS ALGORITHM

In this section we introduce the key aspects of the proposed solution to simultaneously achieve Objective 1 and Objective 2, consisting of a consensus algorithm and the design of a manifold to where the solutions to the system should converge.

First, we augment system (3) with additional state variables (distributed integrators) $\theta_i$, $i \in \mathcal{V}$, with dynamics given by

$$\dot{\theta}_i = -\sum_{j \in \mathcal{N}_i^C} \gamma_{ij} (w_i I_{j} - w_j I_{ij}), \quad (13)$$

where $\mathcal{N}_i^C$ is the set of the DGUs that communicate with the $i$-th DGU, $\gamma_{ij} = \gamma_{ji} \in \mathbb{R}_{>0}$ are additional gain constants, and $w_i, w_j \in \mathbb{R}_{>0}$ are constant weights depending on the DGUs generation capacity. Let $L_c$ denote the (weighted) Laplacian matrix associated with the communication graph, which can be different from the topology of the (reduced) microgrid. Then, the dynamics in (13) can be expressed compactly for all nodes $i \in \mathcal{V}$ as

$$\dot{\theta}_i = -L_c \theta_i, \quad (14)$$

that indeed has the form of a consensus protocol, permitting a steady state where $W \overline{T}_i \in \text{im}(I_n)$ (see also Objective 1). We impose the following restrictions on (14):

Assumption 3: (Controller structure) For all $i \in \mathcal{V}$, the integrators states $\theta_i$ are initialized so such that $1^T_i \theta(0) = 0$. Furthermore, the graph corresponding to the topology of the communication network is undirected and connected.

The most straightforward choice of initialization of the state $\theta_i(0)$, that satisfies Assumption 3, is to initialize all $\theta_i$ to zero, i.e. $\theta(0) = 0$. Whereas connectedness of the communication graph is needed to ensure current sharing among all DGUs, the consequence of the required initialization of $\theta$ is that the
average value of the entries of $\theta$ is preserved and identical to zero for all $t \geq 0$, as proved in the following lemma:

**Lemma 3:** (Preservation of $I^n_n \theta$) Let Assumption 3 hold. Given system (14), the average value $\frac{1}{n} \sum_{i \in V} \theta_i$ is preserved, i.e.,

$$\frac{1}{n} I^n_n \theta(t) = \frac{1}{n} I^n_n \theta(0) \quad \text{for all } t \geq 0. \quad (15)$$

**Proof:** Pre-multiplying both sides of (14) by $I^n_n$ yields

$$I^n_n \dot{\theta} = -I^n_n L_c W I_t = 0,$$  

where $I^n_n L_c = 0$, follows from $L_c$ being the Laplacian matrix associated with an undirected graph. \hfill $\blacksquare$

The fact that $I^n_n \theta(t) = 0$ is essential to the second aspect of the proposed solution, the design of a manifold. Bearing in mind Objective 2, we propose the following desired manifold:

$$\{(I_t, V, I, \theta) : W^{-1}(V - V^*) - \theta = 0\}. \quad (17)$$

Indeed, exploiting the preservation of $I^n_n \theta$, we have on the desired manifold (17), $I^n_n W^{-1} V = I^n_n (\theta + W^{-1} V^*) = I^n_n W^{-1} V^*$. Constraining the solutions to a system to a specific manifold is typical for sliding mode based controllers, and we will discuss some suitable controller designs in the next section.

**Remark 4:** (Plug-and-Play) The main results in this work assume a constant network topology. Nevertheless, an interesting extension is to consider the plugging in or out of various converters. The analysis of the corresponding switched/hybrid system is outside the scope of this work. Here, we merely describe how the required initialization $\theta_0$ should be extended towards the setting of changing topologies, in order to preserve the crucial property $I^n_n \theta = 0$. First, if a new DGU (say DGU$_{n+1}$) wants to join the network, its integrator state is initialized to zero, i.e., $\theta_{n+1}(t_{\text{new}}) = 0$, $t_{\text{new}}$ being the time instant when DGU$_{n+1}$ is plugged-in. Second, if a new DGU (say DGU $i$) is unplugged at the time instant $t_{\text{out}}$, let $\dot{\theta}_i(t) = \theta_i(t_{\text{out}})$ for all $t > t_{\text{out}}$, without re-setting any integrator. If DGU $i$ wants to join again the network at the time instant $t_{\text{in}} > t_{\text{out}}$, the dynamic of $\theta_i$ is described again by (13) for all $t > t_{\text{in}}$. Since $\dot{\theta}_i(t_{\text{in}}) = \theta_i(t_{\text{out}})$, also the plug-in operation occurs without re-setting any integrator state.

V. SLIDING MODE CONTROLLERS

We now propose a Distributed Second Order Sliding Mode (D-SOSM) control law, and a Distributed Third Order Sliding Mode (D-3SM) control law, to steer, in a finite time, the state of system (3), augmented with (14), to the desired manifold (17). As will be discussed in the coming subsections, the choice of the particular control law, D-SOSM or D-3SM, depends on the desired implementation.

First, to facilitate the upcoming discussion, we recall the following definitions that are essential to sliding mode control:

**Definition 1:** (Sliding function) Consider system

$$\dot{x} = \zeta(x, u), \quad (18)$$

with state $x \in \mathbb{R}^n$, and input $u \in \mathbb{R}^m$. The sliding function $\sigma(x) : \mathbb{R}^n \to \mathbb{R}^m$ is a sufficiently smooth output function of system (18).

**Definition 2:** ($r$-sliding manifold) The $r$-sliding manifold\footnote{For the sake of simplicity, the order $r$ of the sliding manifold is omitted in the remainder of this paper.} is given by

$$\{x \in \mathbb{R}^n, u \in \mathbb{R}^m : \sigma = L\sigma = \cdots = L^{(r-1)}\sigma = 0\}, \quad (19)$$

where $L^{(r-1)}\sigma(x)$ is the $(r-1)$-th order Lie derivative of $\sigma(x)$ along the vector field $\zeta(x, u)$. With a slight abuse of notation we also write $L\sigma(x) = \dot{\sigma}(x)$, and $L^{(2)}\sigma(x) = \ddot{\sigma}(x)$.

**Definition 3:** ($r$-order sliding mode (controller)) A $r$-order sliding mode is enforced from $t = T_r \geq 0$, when, starting from an initial condition, the state of (18) reaches the $r$-sliding manifold, and remains there for all $t \geq T_r$. The order of a sliding mode controller is identical to the order of the sliding mode that it is aimed at enforcing.

Bearing in mind the definitions above and the desired manifold (17), we consider the following sliding function $\sigma \in \mathbb{R}^n$:

$$\sigma(V, \theta) = W^{-1}(V - V^*) - \theta. \quad (20)$$

A. Second order SM control: variable switching frequency

Regarding the sliding function (20) as the output function of system (3), (14), it appears that the relative degree\footnote{The relative degree is the minimum order $p$ of the time derivative $\sigma^{(p)}_i, i \in V$, of the sliding variable associated with the $i$-th node in which the control $u_i, i \in V$ explicitly appears.} is two. This implies that a second order sliding mode (SOSM) controller can be naturally applied in order to make the state of the controlled system reach, in a finite time, the sliding manifold \{(I_t, V, I, \theta) : \sigma = \dot{\sigma} = 0\}. According to the SOSM control theory, the auxiliary variables $\xi_1 = \sigma$ and $\xi_2 = \dot{\sigma}$ have to be defined, resulting in the so-called auxiliary system

$$\dot{\xi}_1 = \xi_2, \quad \dot{\xi}_2 = b(I_t, V, I, u) + G_d u, \quad (21)$$

Taking into account the expressions for $\sigma$ and $\dot{\sigma}$, a straightforward calculation shows that, in the auxiliary system (21), the expression for $b \in \mathbb{R}^n$ is given by

$$b = -(W^{-1} C_i^{-1} + L_c W) L_i^{-1} R_i I_t - ((W^{-1} C_i^{-1} + L_c W) L_i^{-1} + W^{-1} C_i^{-1} B L_i^{-1} B^T) V - W^{-1} C_i^{-1} B L_i^{-1} RI - G_a u,$$  

and $G_d, G_a \in \mathbb{R}^{n \times n}$ are

$$G_d = (W^{-1} C_i^{-1} + D_c W)L_i^{-1},$$  

$$G_a = \mathcal{A}_c W L_i^{-1}. \quad (22)$$

Here, $D_c$ and $\mathcal{A}_c$ are the degree matrix and the adjacency matrix of the communication graph, respectively, i.e. $L_c = D_c - \mathcal{A}_c$. We assume that the entries of $b$ and $G_d$ have known bounds for all $i \in V$:

$$|b_i| \leq b_{\text{max}}, \quad (24)$$

$$G_{\text{min},i} \leq G_{d,i} \leq G_{\text{max},i},$$

with $b_{\text{max}}, G_{\text{min},i}$, and $G_{\text{max},i}$ being positive constants. According to the theory underlying the so-called Suboptimal...
SOSSM (SSOSSM) control algorithm [30], the \(i\)-th SOSSM control law, that can be used to steer \(\xi_1\), and \(\xi_2\), to zero in a finite time, even in presence of uncertainties, is given by
\[
u_i = -\mu_i U_{\text{max}}, \quad \text{sgn} \left( \xi_{i1} - \frac{1}{2} \xi_{i1}^{\text{max}} \right),
\]
with
\[
U_{\text{max}} > \max \left( \frac{b_{\text{max}}}{\mu_i G_{\text{min}}}, \frac{4 b_{\text{max}}}{3 G_{\text{min}} - \mu_i G_{\text{max}}} \right),
\]
\[
\mu_i \in (0,1) \cap \left( 0, \frac{3 G_{\text{min}}}{G_{\text{max}}} \right).
\]

Remark 5: (Switching frequency) The discontinuous control signal (25) can be directly used in practice to open and close the switch of the Buck converter. As a result, the Insulated Gate Bipolar Transistors (IGBTs) switching frequency cannot be a-priori fixed and the power losses could be high. Usually, in order to achieve a constant IGBTs switching frequency, Buck converters are controlled by implementing the so-called Pulse Width Modulation (PWM) technique. To do this, a continuous control signal, that represents the so-called duty cycle of the Buck converter, is required.

B. Third Order SM control: duty cycle

To ensure a continuous control input (duty cycle), we adopt the procedure suggested in [30] and first integrate the (discontinuous) control signal generated by a sliding mode controller, yielding for system (3) augmented with (14)
\[
\begin{cases}
L_i \dot{I}_i = -R_i I_i - V + u \\
\dot{C}_i V = I_i + B_I - I_L \\
L \dot{I} = -B^T V - RI \\
\theta = -\mathcal{L}_s W I_t \\
\dot{u} = v,
\end{cases}
\]
where \(v\) is the new (continuous) control input. Note that the input signal to the converter, \(u(t) = \int_0^t v(\tau) d\tau\), is continuous, so that \(u_i\) can be used as duty cycle for the switch of the \(i\)-th Buck converter. A consequence is that the system relative degree (with respect to the new control input \(v\)) is now equal to three, so that we need to rely on a third order sliding mode (3SM) control strategy to reach the sliding manifold \\{\(I_i, V, I, \theta) : \sigma = \dot{\sigma} = \ddot{\sigma} = 0\\} in a finite time. To do so, we define the auxiliary variables \(\xi_1 = \sigma, \xi_2 = \dot{\sigma}\) and \(\xi_3 = \ddot{\sigma}\), and build the auxiliary system as follows
\[
\begin{align*}
\dot{\xi}_1 &= \xi_2 \\
\dot{\xi}_2 &= \xi_3 \\
\dot{\xi}_3 &= b(I_i, V, I, u) + G_d v \\
\dot{u} &= v,
\end{align*}
\]
with \(b\) as in (22), and \(G_d, G_s\) as in (23). Then, we assume that the entries of \(\dot{b}\) can be bounded as
\[
|\dot{b}(\cdot)| \leq \beta_{\text{max}}, \quad \forall i \in \mathcal{V},
\]
where \(\beta_{\text{max}}\) is a known positive constant.

Remark 6: (Uncertainty of \(b, \dot{b}\) and \(G_d\)) The mappings \(b, \dot{b}\) and matrix \(G_d\) are uncertain due to the presence of the unmeasurable current demand \(I_L\) and possible network parameter uncertainties. However, relying on Assumption 1 and observing that \(b\) and \(\dot{b}\) depend on the electric signals related to the finite power of the microgrid, \(b, \dot{b}\) and \(G_d\) are in practice bounded. Generally, the bounds of the unknown quantities can be determined by data analysis and engineering understanding.

Now, the 3SM control law proposed in [32] can be used to steer \(\xi_1, \xi_2\), and \(\xi_3\), \(i \in \mathcal{V}\), to zero in a finite time. It is given by
\[
\begin{align*}
\nu_i &= -\alpha_i \begin{cases}
\nu_1 &= \text{sgn}(\sigma_i) \\
\nu_2 &= \text{sgn} \left( \dot{\sigma}_1 + \frac{\sigma_i^2 + \sigma_i \sigma_1}{2 \alpha_{r_1}} \right) \\
\nu_3 &= \text{sgn}(s_i(\sigma_i))
\end{cases} \quad \sigma_i \in \mathcal{M}_1, / \mathcal{M}_0, \\
\sigma_i \in \mathcal{M}_2, / \mathcal{M}_1, \text{ otherwise},
\end{align*}
\]
where \(\sigma_i = [\sigma_1, \dot{\sigma}_1, \ddot{\sigma}_1]^T\) and
\[
s_i(\sigma_i) = \dot{\sigma}_1 + \frac{\sigma_i^2}{6 \alpha_{r_1}^2} + v_2 \frac{1}{\sqrt{\alpha_{r_1}}} \left( v_2 \sigma_1 + \frac{\dot{\sigma}_1^2}{2 \alpha_{r_1}} \right) + \frac{\dot{\sigma}_1 \sigma_1}{\alpha_{r_1}},
\]
with
\[
\alpha_{r_1} = \alpha_i G_{\text{min}} - \beta_{\text{max}}, > 0.
\]
Then, given the bounds \(G_{\text{min}}, \beta_{\text{max}}\), the control amplitude \(\alpha_i\) is chosen such that \(\alpha_{r_1}\) is positive. The manifolds \(\mathcal{M}_1, \mathcal{M}_2, \mathcal{M}_3\) in (31) are defined as
\[
\begin{align*}
\mathcal{M}_0 &= \{ \sigma_i \in \mathbb{R}^3 : \sigma_i = \dot{\sigma}_i = \ddot{\sigma}_i = 0 \} \\
\mathcal{M}_1 &= \{ \sigma_i \in \mathbb{R}^3 : \sigma_i - \frac{\sigma_i^2}{6 \alpha_{r_1}^2} = 0, \sigma_1 + \frac{\dot{\sigma}_1 \sigma_1}{2 \alpha_{r_1}} = 0 \} \\
\mathcal{M}_2 &= \{ \sigma_i \in \mathbb{R}^3 : s_i(\sigma_i) = 0 \}.
\end{align*}
\]
From (31), one can observe that the controller of DGU \(i\) requires not only \(\sigma_i\), but also \(\dot{\sigma}_i\) and \(\ddot{\sigma}_i\). Yet, according to Assumption 1, only \(I_i\) and \(V_i\) are measurable at the \(i\)-th DGU. Then, one can rely on Levant’s second-order differentiator [33] to retrieve \(\dot{\sigma}_i\) and \(\ddot{\sigma}_i\) in a finite time. Consequently, for system (29), the estimators are given by
\[
\begin{align*}
\dot{\hat{\xi}}_1 &= -\lambda_0 \left| \hat{\xi}_1 - \xi_1 \right|^2 \text{sgn} \left( \hat{\xi}_1 - \xi_1 \right) + \hat{\xi}_2, \\
\dot{\hat{\xi}}_2 &= -\lambda_1 \left| \hat{\xi}_2 - \xi_2 \right|^2 \text{sgn} \left( \hat{\xi}_2 - \xi_2 \right) + \xi_3, \\
\dot{\hat{\xi}}_3 &= -\lambda_2 \text{sgn} \left( \hat{\xi}_3 - \xi_3 \right),
\end{align*}
\]
where \(\hat{\xi}_1 = \hat{\sigma}_1, \hat{\xi}_2 = \dot{\hat{\sigma}}_1\) and \(\hat{\xi}_3 = \ddot{\hat{\sigma}}_1\) are the estimated values of \(\xi_1, = \sigma_1, \xi_2, = \dot{\sigma}_1\) and \(\xi_3, = \ddot{\sigma}_1\), respectively. The estimates obtained via (33) can be used in (31), replacing the original variables. The other parameters are \(\lambda_0 = 3 \Lambda_1^{1/3}, \lambda_1 = 1.5 \Lambda_1^{1/2}, \lambda_2 = 1.1 \Lambda_i, \Lambda_i > 0\), as suggested in [33].
Remark 7: (Scalability and distributed control) Since the selected sliding function (20) is designed by using the additional state \( \theta \) in (14), the overall control scheme is indeed distributed, and only information on generated currents \( I_i \) needs to be shared. More precisely, the controller of the \( i \)-th DGU needs information only from the DGUs that communicate with it. Note that the design of the local controller for each DGU is not based on the knowledge of the whole microgrid, so that the complexity of the control synthesis does not depend on the microgrid size.

Remark 8: (Alternative SM controllers) In this work we rely on the SSO SM control algorithm proposed in [30] and the 3SM control law proposed in [32]. However, the results in this paper are obtained independent of the particular choice of sliding mode controller.

VI. Stability Analysis

In this section we first show that the states of the controlled microgrid are constrained, after a finite time, to the manifold \( \sigma = 0 \), where Objective 2 is achieved. Thereafter, we prove that the solutions to the system, once the sliding manifold is attained, converge exponentially to a constant point, achieving additionally Objective 1.

A. Equivalent reduced order system

As a first step, we study the convergence to the sliding manifold when the SSOSM or the 3SM control law is applied to the system.

Lemma 4: (Convergence to the sliding manifold: SSOSM)
Let Assumption 1 hold. The solutions to system (3) augmented with (14), controlled via the SSOSM control law (25), converge in a finite time \( T_r \), to the sliding manifold \( \{ (I_i, V, I, \theta) : \sigma = \dot{\sigma} = 0 \} \), with \( \sigma \) given by (20).

Proof: Following [30], the application of (25) to each converter guarantees that \( \sigma = \dot{\sigma} = 0 \), for all \( t \geq T_r \).

Lemma 5: (Convergence to the sliding manifold: 3SM)
Let Assumption 1 hold. The solutions to system (3) augmented with (14), controlled via SSOSM control algorithm (29)-(33), converge in a finite time \( T_r \), to the sliding manifold \( \{ (I_i, V, I, \theta) : \sigma = \dot{\sigma} = 0 \} \), with \( \sigma \) given by (20).

Proof: By implementing the Levant’s differentiator (33) in each node, the values of \( \xi_1, \xi_2, \xi_3 \), are estimated in a finite time \( T_{LD} \geq 0 \) [33]. Then, following [32], the application of (31) to each converter guarantees that \( \sigma = \dot{\sigma} = 0 \), for all \( t \geq T_r \geq T_{LD} \).

As we will show in the proof of Theorem 2 in the next subsection, converging to the sliding manifold where \( \sigma = 0 \), is sufficient to conclude that Objective 2 (voltage balancing) is achieved. We postpone the analysis, in order to show additionally convergence to a constant voltage. For the analysis of the system, when the solutions are constrained to the sliding manifold, it is convenient to exploit the so-called system order reduction property, typical of sliding mode control methodology. Indeed, when the state of system (3) augmented with (14) is constrained to the sliding manifold \( \{ (I_i, V, I, \theta) : \sigma = \dot{\sigma} = 0 \} \), with \( \sigma \) given by (20), the controlled system is described by \( 3n + m \) differential equations. Then, it is possible to obtain \( 2n \) state variables depending on the other \( n + m \) ones. The resulting system of order \( n + m \) represents the reduced order system equivalent to the system controlled with a discontinuous law, with the initial condition \( (I_i(T_0), V(T_0), I(T_0), \theta(T_0)) \), when \( \sigma = \dot{\sigma} = 0 \).

Lemma 6: (Equivalent reduced order system) For all \( t \geq T_r \), the dynamics of the controlled system (3) augmented with (14) are given by the following equivalent system of reduced order

\[ C_i \dot{V} = \left( \mathbb{I}_n - (\mathbb{I}_n + C_i W \mathcal{L}_c W^{-1}) \right) BI \]

\[ LI = -B^T V - RI \]

together with the following algebraic relations

\[ \theta = W^{-1}(V - V^*) \]

\[ I_i = (\mathbb{I}_n + C_i W \mathcal{L}_c W^{-1})(-BI + I_L) \].

Proof: Given the sliding function (20), by virtue of Lemma 4 and Lemma 5, the state of system (3) augmented with (14) is constrained to the manifold \( \{ (I_i, V, I, \theta) : \sigma = \dot{\sigma} = 0 \} \), where \( \theta = W^{-1}(V - V^*) \) and \( V = W \theta \). From the latter, one can straightforwardly obtain (36). After substituting expression (36) for \( I_i \) in (3), the dynamics of the voltage \( V \) become as in (34).

B. Exponential convergence and objectives attainment

In the pervious subsection, we established that after a finite time \( T_r \), the dynamics of the controlled microgrid are described by the equivalent system (34). In this subsection we study the convergence properties of this equivalent system. To do so, we rely on the concept of semistability [34], of which we recall the definition for convenience.

Definition 4: (Semistability) Consider the autonomous system

\[ \dot{x}(t) = Ax(t), \]

where \( t \geq 0, x \in \mathbb{R}^n \) and \( A \in \mathbb{R}^{n \times n} \). System (37) is semistable if \( \lim_{t \to \infty} x(t) \) exists for all initial conditions \( x(0) \).

Furthermore, the following lemma turns out to be useful in the upcoming analysis:

Lemma 7: Given a positive definite matrix \( P \in \mathbb{R}^{n \times n} \) and a positive semidefinite matrix \( Q \in \mathbb{R}^{n \times n} \), then

\[ P - (P^{-1} + Q)^{-1} \geq 0. \]

Proof: Let \( \tilde{Q} = P^{\frac{1}{2}} Q P^{\frac{1}{2}} \). Clearly, \( \tilde{Q} \geq 0 \), and \( \mathbb{I}_n + \tilde{Q} > 0 \). Then,

\[ P - (P^{-1} + Q)^{-1} = P^{\frac{1}{2}} \left[ \mathbb{I}_n - (\mathbb{I}_n + \tilde{Q})^{-1} \right] P^{\frac{1}{2}} \]

is a positive semidefinite matrix if and only if \( \mathbb{I}_n - (\mathbb{I}_n + \tilde{Q})^{-1} = \tilde{Q}(\mathbb{I}_n + \tilde{Q})^{-1} \geq 0 \). Observing that \( (\mathbb{I}_n + \tilde{Q})^{-1} \geq 0 \), it yields

\[ \tilde{Q}(\mathbb{I}_n + \tilde{Q})^{-1} (\mathbb{I}_n + \tilde{Q})^{\frac{1}{2}} \tilde{Q}(\mathbb{I}_n + \tilde{Q})^{-\frac{1}{2}} \geq 0, \]

which completes the proof.
Next, we show that the line currents $I$ converge to a constant value.

**Lemma 8: (Convergence of $I$)** Let Assumptions 1 and 2 hold. Given the equivalent reduced order system (34), $\lim_{t \to \infty} I(t)$ exists for all initial conditions $I(T_r)$.

**Proof:** Let $\dot{V} = V - \bar{V}$ and $\bar{I} = I - \bar{I}$ be the error given by the difference between the state of system (34) and a steady state value. Then, the dynamics of the corresponding error system are given by

$$\begin{align*}
C_i \dot{\bar{I}} &= \left( I_n - (I_n + C_i W L_i W)^{-1} \right) B \bar{I}, \\
\dot{\bar{I}} &= - B^T \dot{V} - R \bar{I},
\end{align*}$$

From (41), we obtain

$$\begin{align*}
\dot{V} &= C_i^{-1} \left( I_n - (I_n + C_i W L_i W)^{-1} \right) B \bar{I} \\
&= \left( C_i^{-1} - (C_i + C_i W L_i W C_i)^{-1} \right) B \bar{I},
\end{align*}$$

and

$$\begin{align*}
\dot{\bar{I}} &= \dot{\bar{I}} + B^T \dot{V} = 0.
\end{align*}$$

Substituting expression (42) for $\dot{V}$ in (43) leads to

$$
L \dot{\bar{I}} + R \bar{I} + B^T \left( C_i^{-1} - (C_i + C_i W L_i W C_i)^{-1} \right) B \bar{I} = 0.
$$

Since, by virtue of Lemma 7 (with $P = C_i^{-1}, Q = C_i W L_i W C_i$), $C_i^{-1} - (C_i + C_i W L_i W C_i)^{-1} \geq 0$, then we also have $K = B^T(C_i^{-1} - (C_i + C_i W L_i W C_i)^{-1}) \geq 0$. According to [34, Corollary 2], system (44) is semistable (see Definition 4) if and only if

$$
\begin{align*}
\text{rank} \begin{bmatrix} R \\
R(L^{-1}K) \\
\vdots \\
R(L^{-1}K)^{m-1} \end{bmatrix} &= m.
\end{align*}
$$

Since $R$ is a positive definite $m \times m$ diagonal matrix it can be readily confirmed that condition (45) holds, such that system (44) is indeed semistable. Since $T$ is a constant vector, it immediately follows that $\lim_{t \to \infty} I(t)$ exists. $\blacksquare$

Lemma 8 established that $\lim_{t \to \infty} I(t)$ exists for all initial conditions $I(T_r)$. This result can now be exploited to show that also the voltages converge to constant values.

**Lemma 9: (Convergence of $V$)** Let Assumptions 1–3 hold. Given the equivalent reduced order system (34), $\lim_{t \to \infty} V(t)$ exists for all initial conditions $V(T_r)$.

**Proof:** Exploiting the convergence of $I$ to a constant vector (see Lemma 8), from (43) we have

$$
\lim_{t \to \infty} B^T \dot{V}(t) = 0,
$$

implying that

$$
\lim_{t \to \infty} \dot{V}(t) = \eta_n \kappa,
$$

with $\kappa \in \mathbb{R}$. By virtue of Lemma 3 and Lemma 4 or Lemma 5, for all $t \geq T_r$, we also have

$$
\eta_n W^{-1} V(t) = \eta_n (\theta + W^{-1} V^*) = \eta_n \theta(0) + \eta_n W^{-1} V^*.
$$

Taking the derivative with respect to time on both sides of (48), it follows that $\dot{\eta}_n W^{-1} \dot{V}(t) = 0$ for all $t \geq T_r$. Exploiting (47), we obtain

$$
\lim_{t \to \infty} \eta_n W^{-1} \dot{V}(t) = \lim_{t \to \infty} \dot{V}(t) = 0,
$$

which implies $\kappa = 0$ and consequently that $\lim_{t \to \infty} V(t)$ exists for all initial conditions $V(T_r)$. $\blacksquare$

We are now ready to establish the first main result of this paper.

**Theorem 1: (Achieving current sharing)** Let Assumptions 1–3 hold. Consider system (3), (14), controlled with the proposed distributed SSOSM (Subsection V-A) or 3SM (Subsection V-B) control scheme. Then, the generated currents $I_i(t)$ converge, after a finite time, exponentially to $W^{-1} \eta_n I_{nI}(1) T_L / (\eta_n W^{-1} \eta_n)$, achieving proportional current sharing.

**Proof:** According to Lemma 6, for all $t \geq T_r$, the dynamics of the controlled system (3), (14) are given by the autonomous system (34) together with the algebraic equations (35) and (36). Bearing in mind the results proved in Lemma 8 and Lemma 9, the dynamics of the line current $I$ and the voltage $V$ are semistable. From the algebraic equations (35) and (36), it follows that $\lim_{t \to \infty} \dot{\theta}(t)$ and $\lim_{t \to \infty} I_i(t)$ exist as well. Since (34) is linear and $\ker(C_i) = \text{im}(\eta_n)$, (14) implies that the vector $I_i(t)$, with initial condition $I_i(T_r)$, converges exponentially to a constant vector, achieving proportional current sharing.

We now proceed with establishing the second main result of this paper.

**Theorem 2: (Achieving voltage balancing)** Let Assumptions 1–3 hold. Consider system (3), (14), controlled with the proposed distributed SSOSM (Subsection V-A) or 3SM (Subsection V-B) control scheme. Then, given a desired reference vector $V^*$, the voltages $V(t)$ satisfy $\eta_n W^{-1} \dot{V}(t) = 1 \eta_n W^{-1} V^*$ for all $t \geq T_r$, with $T_r$ a finite time.

**Proof:** Following Lemma 4 or Lemma 5, for all $t \geq T_r$, the equality $\dot{W}^{-1} V(t) = W^{-1} V^* + \theta(t)$ holds. Pre-multiplying both sides by $\eta_n W^{-1}$ yields $\eta_n W^{-1} \dot{V}(t) = \eta_n W^{-1} V^* + \eta_n \theta(t)$. Due to Assumption 3 and by virtue of Lemma 3, one has that $\eta_n \theta(t) = \eta_n \theta(0) = 0$. Then, one can conclude that voltage balancing is achieved for all $t \geq T_r$.

**Remark 9: (Robustness to failed communication)** The proposed control scheme is distributed and as such requires a communication network to share information on the generated currents. However, note that the integrators $\theta$ in (14) are not needed to regulate the voltages in the microgrid to their desired values, but are only required to achieve current sharing and voltage balancing. In fact, by omitting the variable $\theta$ in the analysis, the controlled microgrid converges, in a finite time, to the manifold $\sigma = 0$, where $V = V^*$, as shown in [14]. Moreover, considering constant value of $\theta_i$ (e.g. after the plug-out of the DGU $i$), the controlled DGU $i$ converges, in a finite time, to the manifold $\sigma_i = 0$, where $V_i = V_i^* + w_i \bar{\theta}_i$. 


The PCC voltages and the generated currents are reported together with the control signals generated by the 3SM control algorithm (31). Note that the 3SM controllers, which require only local measurements of $V_i$ and information on $I_i$ from neighbours over the communication network, generate control signals that are equal to the optimal feedforward input (7), without exact knowledge on the network parameters and the current demand $I_L$.

### B. Scenario 2: opening of a distribution line

In the second scenario, we investigate the performance of the proposed controllers when a distribution line is opened (e.g., due to an electric fault). The system is initially at the steady state, and at the time instant $t = 0.4$ s, the distribution line interconnecting the DGUs 1 and 4 is opened. Then, consider a current demand variation as in Scenario 1. The PCC voltages and the generated currents are illustrated in Figure 6. One can appreciate that the weighted average of the PCC voltages (denoted by $V_{av}$) is always equal to the weighted average of the corresponding references (see Objective 2), and the current generated by each DGU converges to the desired value, achieving proportional current sharing (see Objective 1). Moreover, in Figure 5 the currents shared among the DGUs are reported together with the control signals generated by the 3SM control algorithm (31).

### C. Scenario 3: plug-out and plug-in of a DGU

In the third scenario, we investigate the Plug-and-Play (PnP) capabilities of the proposed controllers. For the sake of clarity, in this scenario and the next one we consider equal current sharing among the DGUs. The system is initially at the steady state, and at the time instant $t = 0.4$ s, the DGU 4 is disconnected from the considered DC network (in this configuration the impedance of the line interconnecting DGU 1 and DGU 3 is equal to the sum of the line impedances $Z_{14}$ and $Z_{34}$). After a current demand variation as in Scenario 1, at the time instant $t = 1.4$ s, the DGU 4 is reconnected to the DC network. The PCC voltages and the generated currents are illustrated in Figure 8. One can appreciate that the arithmetic average of the PCC voltages (denoted by $V_{av}$) is equal to the arithmetic average of the corresponding references, even after disconnecting the DGU 4. Moreover, when the DGU 4 operates isolated from the considered DC network, equal current sharing is achieved only among the DGUs 1, 2 and 3, while the DGU 4 supplies its local load. However, when the DGU 4 is reconnected to the DC network, current sharing among all the DGUs is again reestablished. Moreover, in Figure 9 the currents shared among the DGUs are reported together with the control signals generated by the 3SM control algorithm (31). Note that, when the DGU 4 is isolated from the network, the comparison between $u_4$ and the corresponding optimal feedforward input loses its meaning.
Fig. 3. From the left: the configurations of the considered microgrid implemented in Scenario 1, Scenario 2, Scenario 3 and Scenario 4, respectively. $Z_{ij}$ denotes the resistive-inductive impedance of the distribution line interconnecting DGU $i$ with DGU $j$. Components that are failing/removed during the simulation are colored red.

Fig. 4. Scenario 1. From the top: voltage at the PCC of each DGU together with its weighted average value (dashed line); generated currents together with the corresponding values (dashed lines) that allow to achieve proportional current sharing.

Fig. 5. Scenario 1. From the top: currents shared among the DGUs through the lines; control inputs $u_i(\tau) = \int_0^\tau v_i(\tau)d\tau$, $v_i$ as in (31), together with the optimal feedforward inputs (7) indicated by the dashed lines.

Fig. 6. Scenario 2. From the top: voltage at the PCC of each DGU together with its weighted average value (dashed line); generated currents together with the corresponding values (dashed lines) that allow to achieve proportional current sharing.

Fig. 7. Scenario 2. From the top: currents shared among the DGUs through the lines; control inputs $u_i(\tau) = \int_0^\tau v_i(\tau)d\tau$, $v_i$ as in (31), together with the optimal feedforward inputs (7) indicated by the dashed lines.
D. Scenario 4: failing of a communication link

In the last scenario, we investigate the robustness of the proposed controllers to failed communication. The system is initially at the steady state, and at the time instant $t = 0.4$ s, the communication between DGU 3 and DGU 4 is interrupted. We observe that as long as the demand does not change, current sharing among all the DGUs in maintained. The PCC voltages and the generated currents are illustrated in Figure 10. One can note that after a current demand variation (see Table II), equal current sharing is achieved only among the DGUs 1, 2 and 3, while the DGU 4 generates a current such that the voltage at node 4 is kept constant. One can appreciate that the arithmetic average of the PCC voltages (denoted by $V_{av}$) is equal to the arithmetic average of the corresponding references, even after interrupting the communication between DGU 3 and DGU 4. Moreover, in Figure 11 the currents shared among the DGUs are reported together with the control signals generated by the 3SM control algorithm (31). Note that, when the communication with DGU 4 fails, the comparison between $u_4$ and the corresponding optimal feedforward input loses its meaning.

Even if IEEE Standards or guidelines for DC power distribution networks do not exist yet (to the best of our knowledge), it is usually required in practical cases that the voltage deviations are within the 5% of the desired value (see for instance [24]). In all the previous scenarios, the voltage at the PCC of each DGU is within the range $380 \pm 1$ V, implying that the voltage deviations are less than the 0.3% of the nominal value $V^* = 380$ V, even during transients and critical conditions.
In this paper we design a distributed control algorithm, obtaining proportional current sharing and voltage regulation in DC microgrids. Its convergence properties are analytically investigated. The proposed control scheme exploits a communication network to achieve proportional current sharing using a consensus-like algorithm. Another useful feature of the proposed control scheme is that the weighted average voltage of the microgrid converges to the weighted average of the voltage references, independently of the initial voltage conditions. The latter is achieved by constraining the system to a suitable manifold. To ensure that the desired manifold is reached in a finite time, even in presence of modelling uncertainties, two sliding mode control strategies are proposed, that provide the switching frequencies or the duty cycle of the power converters. An extensive simulation analysis is also provided, considering different and realistic scenarios. The proposed controllers show satisfactory closed-loop performance and Plug-and-Play capabilities, even in presence of uncertainties, topology changes and communication failings. Interesting future research includes the design of distributed controllers aimed at guaranteeing power sharing and studying the stability analysis of the resulting nonlinear system.

REFERENCES